B.Sc.ELECTRONICS

Syllabus

AFFILIATED COLLEGES

Program Code: 22M

2023-2024



BHARATHIAR UNIVERSITY

A State University, Accredited with "A" Grade by NAAC, Ranked 13th among Indian Universities by MHRD-NIRF,

Program Ed	lucational Objectives (PEOs)					
	The B. Sc. Electronics program describe accomplishments that graduates are expected to attain within five to seven years after graduation					
PEO1	Provide graduates with a strong foundation in Electronics domain and to enable them to devise and deliver efficient solutions to challenging problems in Electronics, Communications and allied disciplines.					
PEO2	Impart analytic and thinking skills to develop initiatives and innovative ideas for R&D, Industry and societal requirements.					
PEO3	Provide sound theoretical and practical knowledge of Electronics, managerial and entrepreneurial skills to enable students to contribute to the wellbeing of society with a global outlook.					
PEO4	Inculcate qualities of teamwork as well as social, interpersonal and leadership skills and an ability to adapt to evolving professional environments in the domains of engineering and technology.					
PEO5	Motivate graduates to become good human beings and responsible citizens for the overall welfare of the society.					
PEO6	Develop attitude in lifelong learning, applying and adapting new ideas and technologies as their field evolves.					
PEO7	To prepare graduates who will have knowledge, ability and courage to pursue higher studies and research.					

Program Sp	Program Specific Outcomes (PSOs)					
After the suc	After the successful completion of B.Sc. Electronics program, the students are expected to					
PSO1 Demonstrate proficiency in use of software and hardware required to practice electronics and communication profession.						
PSO2	Graduates will be able to apply fundamentals of electronics in various domains of analog and digital systems					
PSO3	Apprehend and analyses specific engineering problems of communication, electronic circuits, computer programming, embedded systems, VLSI design and semiconductor technology by applying the knowledge of basic sciences, engineering mathematics and engineering fundamentals.					
PSO4	Ability to communicate effectively with excellent interpersonal skills and demonstrate the practice of professional ethics for societal benefit					
PSO5	Graduates will be able to apply fundamentals of electronics in various domains of analog and digital systems.					
PSO6	Use embedded system concepts for developing IoT applications					

Program Ou	itcomes (POs)
On successfu	al completion of the B. Sc. Electronics program
PO1	Engineering knowledge : Apply the knowledge of mathematics, Science, Engineering fundamentals and an engineering specialization to the solution of complex engineering problems
PO2	Problem analysis: Identify, formulate, review research literature and analyses complex engineering problems reaching substantiated conclusion using principles of mathematics and Engineering sciences
PO3	Design/Development of solutions: Design solutions for complex Engineering problems and design system components or processes that meet the specified needs with appropriate consideration for the public health and safety, and the cultural, societal and environmental conditions.
PO4	Conduct investigation of complex problems: Use research based knowledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the information to provide valid conclusions.
PO5	Modern tool usage: Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modeling to complex engineering activities with an understanding of the limitations
PO6	The engineer and society: Apply reasoning informed by the contextual knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the professional engineering practice.
PO7	Environment and Sustainability: Understand the impact of the professional engineering solution in societal and environmental contexts, and demonstrate the knowledge of and need fir sustainable development
PO8	Ethics: Apply ethical principles and commit to professional ethics and responsibilities and norms of the engineering practice.
PO9	Individual and team work: Function effectively as an individual, an as a member or leader in diverse teams, and in multidisciplinary settings.
PO10	Life-Long learning : Recognize the need for and have the preparation and ability to engage in independent and life-long learning in the broadest context of technological change.

BHARATHIAR UNIVERSITY: COIMBATORE 641 046

B. Sc. Electronics Curriculum (University Affiliated Colleges) (For the students admitted during the academic year 2023 – 24 onwards)

Course	Title of the Course	Credits	Но	urs	Maximum Marks		
Code			Theory	Practical	CIA	ESE	TOTAL
			A CORED				
		FIRST SE					
11T	Language –I	4	4	- (52a.5)	25	75	100
12E	English – I	4	4	7	25	75	100
13A	Core Paper I : Basic Electronics	4	6	ું હ	25	75	100
	Core Practical I: Basic Electronics Lab			4	13	-	-
	Core Practical II : Semiconductor Devices Lab			4	31-10	-	-
1AA	AlliedI Mathematics-I	4	6		25	75	100
1FA	Environmental Studies *	2	2	7	B.	50	50
	Total	18	T 22	L 08	100	350	450
	S	ECOND S	EMESTE	CR.	7	970	
21T	Language – II	4	4	100	25	75	100
22E	English – II	2	4	-	25	25	50 @
23A	Core Paper II: Semiconductor Devices	4	6	ाएं के की	25	75	100
23P	Core Practical I: Basic Electronics Lab	DUCATE	LO ELEAN	E 3	25	75	100
23Q	Core Practical II: Semiconductor Devices Lab	4	-	3	25	75	100
2AA	Allied : II Mathematics—II	4	6	-	25	75	100
2FB	Value Education- Human Rights *	2	2	-	-	50	50
2NM	NAAN MUTHALVAN Generic Name: Language Proficiency for Employability Course Name: Effective English	2	2	-	25	25	50#
Swatch Bharat	t- Summer internship **						
	Total	26	T 24	L 06	175	475	650

		THIRD	SEMESTI	ER					
31T	Language – III	4	4	-	25	75	100		
32E	English – III	4	4	-	25	75	100		
33A	Core Paper III: Digital Principles and Applications	4	3	-	25	75	100		
33B	Core Paper IV: Electronic Circuits	4	3	-	25	75	100		
3AD	Allied : III Object Oriented Programming using C++	3	3.1	65.49	20	55	75		
3ZA	Skill Based Subject : I Bio Medical Instrumentation	3	3	(8)	20	55	75		
	Core Practical: III Digital Electronics and Linear IC's Lab - III	11		3	14 15 16 16 16 16 16 16 16 16 16 16 16 16 16	-	-		
	Core Practical: IV Electronics Circuits and Communication Lab -IV	Was in the	-	3		_	-		
3NM	NAAN MUDHALVAN Microsoft Office Fundamentals	2	2	3	25	25	50#		
3FB / 3FC	Tamil **/ Advanced Tamil*(OR) Non-major elective - I (Yoga for Human excellence * Womens Rights*)	2 3) Die 10	2 Calimban	יוות פייוניים	की एकार्क	50	50		
	Total	26 _{FDU}	T 24	Lab- 06	165	485	650		
	FOURTH SEMESTER								
41T	Language – IV	4	4	-	25	75	100		
42E	English – IV	4	4	-	25	75	100		

43A	Core Paper V: IC's and Instrumentation	4	5	-	25	75	100
4AD	Allied : IV Python Programming	4	5		25	75	100
4NM	Skill Based Subject :II NAAN MUDHALVAN CourseGenericName : Digital skills for Employability Course Name: Office Fundamentals	2	2	-	25	25	50#
43P	Core Practical: III Digital Electronics and Linear IC's Lab	3	-	3	30	45	75
43Q	Core Practical: IV Electronics Circuits and Communication Lab -IV	3	-	3	30	45	75
43R	ALLIED Practical: V Computer Programming Lab	2	-	2	20	30	50
4FB/ 4FE	Tamil**/Adva nced Tamil * (OR)Non- major elective- II(General Awareness *)	2	2	-	-	50	50
Total	<u>′</u>	28	T 22	Lab 08	205	495	700
		FIF	ГН ЅЕМЕ	STER	'		1
53A	Core Paper VI: 8051 Microcontroller and its Applications	4	4	-	25	75	100
53B	Core Paper VII: Internet of Things	4	4	-	25	75	100

B.Sc. Electronics 2023-24 onwards-Affiliated Colleges - AnnexureNo.30(a)(1) SCAADATED:18.06.2024 ,REVISED JUNE 2024

	T		T	T.	T	T	
5EA/	Elective I				20		_
5EB/		3	4		20	55	75
5EC/							
5ED							
5EE/	Elective II				_		
5EF/		3	4		20	55	75
5EG/							
5EH							
					<u> </u>		
	Core Practical VI:						
	Internet of Things	-	-	3	-	-	
	System Design Lab						
	Comp D4' - 13777	1			1		
	Core Practical VII:			2			
	Industrial and Power	_	_	3	-	-	-
	Electronics Lab						
	Core Practical VIII:	1		3			
		-	-	٥	-	-	-
	Microcontroller Lab						
	NAAN MUDHALVAN	<u> </u>					
5NM	Generic Name: Technical	2	2	_	25	25	50#
JI VIVI	skills for Employability Course Name: Data	<i>-</i>					2011
	Analytics with Tableau						
	Skill based subject – III				_		
5ZC	Visual Programming	3	3		20	55	75
	Total	19	Theory	Lab 09	135	340	475
	1 71111		21	2000)	133	210	.,,
		6	M. History		(6)		
		- CI	XTH SEM	ESTED			
	Core Paper VIII:	SI	AITISEN	LSIEK	3 6		
63A	Design with PIC	4	6		25	75	100
USA	Microcontroller	4	0		2.5	13	100
	wherocontroller	A LANGE	1				
6EI/	Elective III	The same	day y	300	3.7		
6EI/	Elective III	3	6		20	55	75
6EK/	100	E		E A	120		7.5
6EL		30	I		05	2	/
021	Core Practical	400	Bereit	THE PARTY OF		50	
63P	VI: Internet of	4	VAR	3	25	75	100
0.51	Things System		Coimba	tore _		,5	100
	Design Lab	S ()			481		
	Design Lau	43/15		The state of the s			

EDUCATE TO ELEVATE

SCAADATED:18.06.2024 ,REVISED JUNE 2024

	GrandTotal	144					3600
	Total		18			.,,	
		27	Theory	Lab 12	185	490	675
6NM	NAAN MUDHALVAN SALES FORCE	2	2	-	25	25	50#
67A	** Extension Activities	2	2	-	-	50	50
6ZP	Skill based Subject – IV Practical Visual Programming	2	-	3	20	30	50
67V	PROJECT	2	2	-	20	30	50
63R	Core Practical VIII: 8051 Microcontroller Lab	4	-	3	25	75	100
63Q	Core Practical VII: Industrial and Power Electronics Lab	4	-	3	25	75	100

For Project report *30 marks and viva-voce *20 marks

^{*} No Continuous Internal Assessment (CIA) . Only University Examinations

^{**} No University Examinations. Only Continuous Internal Assessment (CIA).

[@] University semester examination will be conducted for 50 marks(As per the existing pattern of examination) and the marks will be converted to 25 marks

[#] Naan Mudhalvan Course :CEE will be assessed by Industry for 25 marks and CIA will be done by the course teacher

② @ English II- University semester examination will be conducted for 50 marks (As per existing pattern of Examination) and it will be converted for 25 marks.

^{**} Swatch Bharat Summer internship- extra 2 credits would be given. It is mandatory

List of Elective papers (Colleges can choose any one of the paper as electives)				
A PCB Design and Fabrication				
Elective – I	C D	Advanced Communication Systems		
	C	Artificial Intelligence		
	D	Advanced Computer Architecture		
	E	Arduino with Sensors		
Elective – II F		Microwave and Fiber Optic Communication		
G Automotive Electronics		Automotive Electronics		
	Н	Satellite Communications		

	I	Industrial and Power Electronics
Elective – III	J	Robotics
	K	Programmable Logic Controllers
	L	VLSI Design



	Government of Tamil Nadu " NAAN MUDHALVAN SCHEME" www. naanmudhalvan.tn.gov.in List of Course for each semester
Semester	Name of The Courses
II	Effective English
III	Microsoft Office Fundamentals
IV	Digital Skills For Employability
V	Technical Skills for Employability
VI	Emerging Technology For Employability III

NAAN MUDHALVAN SCHEME SYLLABUS

http://kb.naanmudhalvan.in/Bharathiar_University_(BU)



Course code 13A	BASIC ELECTRONICS	L	T	P	C	
Core/Elective/ Supportive:	Core Paper I	5			4	
Pre-requisite	Higher secondary physics	Sylla Vers	bus 2	2023	-24	
Course Objectives:						
The main objectives of the						
	r with fundamentals of electronic components					
	nmon electronic components c circuits to perform realistic tasks					
_						
Expected Course Outcor						
	etion of the course, student will be able to:					
	electronic components			K		
2 Understand the basic electronic components						
3 Differentiate and demonstrate the voltage and current source.						
4 Apply the electronic components in network theorems.						
•	use the electronic components			K	4	
K1 - Remember; K2 - U	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 – C	reate			
				1		
Unit:1	RESISTORS & INDUCTORS ed, Variable - Brief mention of their Construction a			hour		
Of Electromagnetic Indu Testing of Resistance and	ed, Variable- Self and Mutual Inductance-Faraday's Inction-Energy Stored In An Inductor-Inductance In Standard Unductance using Multimeter.		And F	Paral	lel-	
Unit:2	CAPACITORS			houi		
Capacitors: Air, Paper, Application- Connecting Capacitors- Testing of Ca Millimeters. Unit:3	Mica, Teflon, Ceramic, Plastic and Electrolytic Capacitors in Series and Parallel - Factors Gove apacitors Using ELECTRICAL ELEMENTS AND CIRCUITS Electric Current-Electromotive Force - Ohms Law	Cons	tructi the V	on a alue	and of s	
Kirchoff'sCurrent Law-Parallel Circuits-Concep	Analysis of Resistance in Series Circuits, Parallel t of Voltage Source and Current Source-Voltage Sel-Simple Problems in DC Circuits.	Circuit	s and	l Ser	ries	
Unit:4	NETWORK THEOREMS		15	hour	rs .	
Conversion - Conversion	- Thevenin Theorem Norton's Theorem - To of Voltage and Current Sources - Millman's Theorem - Simple Problems in DC Circumstances.	em - S				
Unit:5	AC CIRCUITS		15	hour	'S	
Circuits with XL Alone -	Wave-RMSValue-AverageValue-ACCircuitswithResis - Circuits with XC Alone - Series Reactance And Resister - Series Parallel Reactance and Resistance - Reactance - R	stance -				

Pasanant Circuit Parallal	Resonant Circuit - Passive Filters: Low Pass Filters, H	Jigh
		ngn
Pass Filters, Band Pass Filt	ers, Band Rejection Filters	
	Total Lecture hours	75 hours
Text Book(s)		
1 S.Salivahanan, N.Sures	sh Kumar, A. Vallavaraj "ELECTRONIC DEVICES	AND
CIRCUITS"-	, <u>, , , , , , , , , , , , , , , , , , </u>	
2 B.V.NarayanaRao— PR	INCIPLES OFELECTRONICS, WileyEasternLimit	ted, 1992
		,
Reference Books		
1 D I Thoroic "DASIC	ELECTRONICS-SOLID STATE DEVICES",S.Ch	and Company
J ,	· · · · · · · · · · · · · · · · · · ·	
	ELECTRONICS"-Tata McGraw-Hill Publishing Co	mpany Limited,
9thEdition.		
Related Online Contents	[MOOC, SWAYAM, NPTEL, Websites etc.]	
1 https://nptel.ac.in/cours	es/108/104/108104139/	
2 https://nptel.ac.in/cours	es/108/101/108101091/	
3 https://www.youtube.co	om/playlist?list=PLFF553CED56CDE25D	
4 https://www.youtube.co	om/watch?v=w8Dq8blTmSA	
Course Designed By:		
Dr.K. <mark>Venmath</mark>	i ,Assistant professor,L.R.G.Govt Arts College for Wo	omen, Tirupur &

Mappir	Mapping with Programme Outcomes											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	S	υ s	M	L	L	L	L	M	S	M		
CO2	L	L	L	L	L	M	M	S	M	S		
CO3	M	M	S	L	M	S	L GO S	L	M	M		
CO4	M	L	L	EDUCAT	E 10 EF	EVALE	L	L	M	S		
CO5	L	M	S	S	M	L	L	L	M	M		

S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

^{*}S-Strong; M-Medium; L-Low

SCAADATED:18.06.2024, **REVISED JUNE 2024**

90 Hours

Course code 23P	BASIC ELECTRONICS LAB	L	Т	P	С
Core/Elective/ Supportive:	Core Practical I			3	4
Pre-requisite	Basic Electronics theory	Sylla Vers		2023	-24

Course Objectives:

The main objectives of this course are to:

- ❖ To understand the fundamental principles of circuit theory
- ❖ To make use of circuit laws and theorems and measuring thecircuit parameters.

Expected Course Outcomes: On the successful completion of the course, student will be able to: 1 Apply the concept of basic circuit and theorems K3 2 Simplify the circuits using series and parallel equivalents and using Thevenin's and Norton's equivalent circuits. 3 Design resonance circuits. K4 4 Use the oscilloscope for the display and measurements of signals. K2 5 Analyze Various Theorems with different resistance values K4

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

BASIC ELECTRONICS LAB

(Any 16 Experiments)

- 1. Study of Multimeter Checking of Components
- 2. Measurement of Amplitude, Frequency & Phase Differenceusing CRO
- 3. Verification of Ohm's Law
- 4. Voltage sources in Series, Parallel and Series-Parallel
- 5. Resistance in Series, Parallel and Series-Parallel
- 6. Voltage and Current Dividers
- 7. Verification of Kirchhoff's Law
- 8. Wheatstone Bridge
- 9. Verification of Norton's Theorem
- 10. Verification of Thevenin's Theorem
- 11. Verification of Millman's Theorem
- 12. Verification of Superposition Theorem
- 13. LCR Bridge
- 14. SeriesResonance Circuit
- 15. ParallelResonance Circuit
- 16. Transient Response of RC Circuit
- 17. Transient Response of RL Circuit
- 18. Capacitors & Inductors in Series & Parallel
- 19. Frequency Response of R, L&C
- 20. Low Pass Filter & HighPass Filter
- 21. Band pass and BandRejection Filter
- 22. Verification of Maximum PowerTransfer Theorem
- 23. Measurement of resistance and capacitance in series and parallel

- https://nptel.ac.in/courses/122/106/122106025/ https://nptel.ac.in/courses/122/106/122106026/

Course Designed By: K.Manikantan, Assistant Professor, Government Arts College, Ooty.& Dr.N Om Muruga, Assistant Professor, Government Arts College, Ooty.

Mappi	Mapping with Programme Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10			
CO1	L	L	M	L	L	L	L	M	S	M			
CO3	L	M	M	M	L	M	L	S	M	L			
CO3	M	L	S	L	L	S	L	L	L	M			
CO4	M	M	L	L	a L	S	M	L	M	L			
CO5	L	M	S	M	M	M	L	L	M	M			

*S-Strong; M-Medium; L-Low

Course code 23Q	SEMICONDUCTOR DEVICES LAB	L	T	P	C
Core/Elective/ Supportive:	Core Practical II			3	4
Pre-requisite:	Higher secondary physics	Sylla Vers		2023	3-24

Course Objectives:

The main objectives of this course are to:

- ❖ To understand and experiment the basic parameters of electronic devices.
- ❖ To construct few applications using semiconductor devices.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

	the successful completion of the course, student will be use to.	
1	Experiment the fundamental operations of the main semiconductor electronic devices.	К3
2	Design and construct electronic circuits using semiconductor devices.	К3
3	Understand the transistor characteristics	K2
4	Understand the characteristics of LDR and solar cell	K2
5	Apply Various transistor characteristics in applications.	К3

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

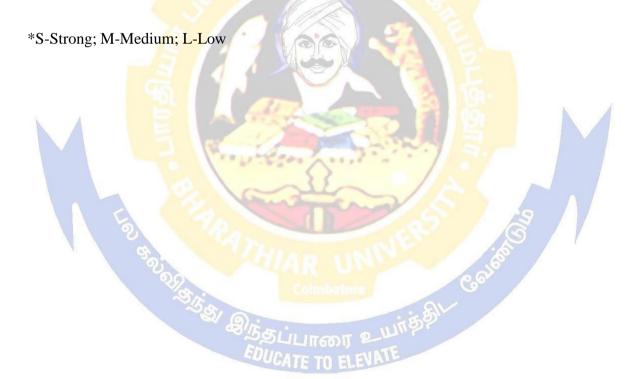
UNIT I	SEMICONDUCTOR DEVICES LAB	90 Hours
	All the same of th	

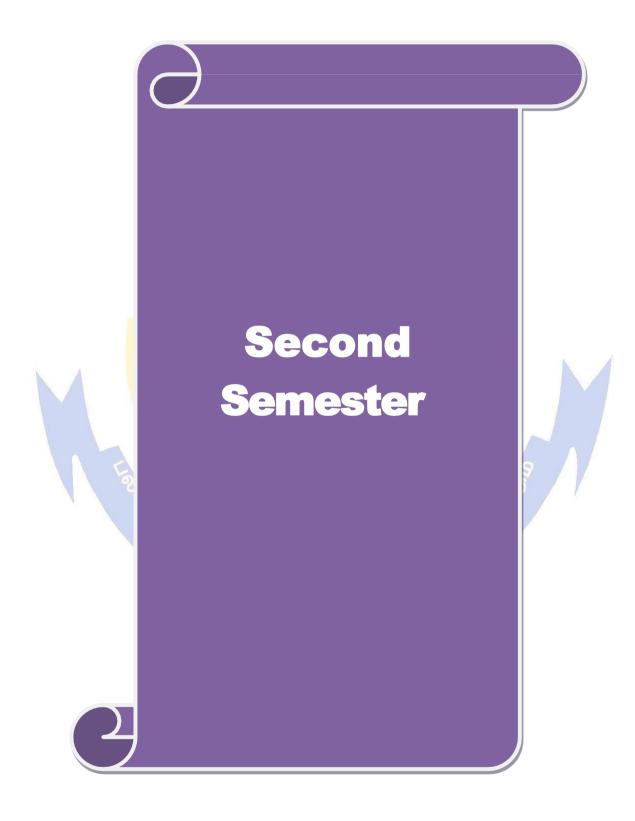
(Any 16 Experiments)

- 1. Band Gap Energy of Silicon / Germanium Diode
- 2. V-I Characteristics of Junction Diode
- 3. V-I Characteristics of Zener Diode
- 4. Transistor Characteristics of CE Configuration
- 5. Transistor Characteristics of CB Configuration
- 6. Transistor Characteristics of CC Configuration
- 7. Clipping Circuits
- 8. Clamping Circuits
- 9. Measurement of Stability Factor of Fixed Bias
- 10. Measurement of Stability Factor of Self Bias
- 11. V-I Characteristics of JFET
- 12. V-I Characteristics of UJT
- 13. UJTas Oscillator
- 14. Characteristics of Solar Cell
- 15. Study of IR(Tx&Rx)
- 16. Study of LED and 7Segment display
- 17. Temperature Co-efficient of Junction Diode
- 18. Zener as a Voltage regulator
- 19. ON / OFF control of relay using Opto-Couplers
- 20. Characteristics of SCR
- 21. TRIAC Characteristics

Course Designed By: K.Manikantan, Assistant Professor, Government Arts College, Ooty. & Dr.N Om Muruga, Assistant Professor, Government Arts College, Ooty.

	Mapping with Programme Outcomes											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	L	L	M	L	L	L	L	M	S	M		
CO3	L	M	M	M	L	M	L	S	M	L		
CO3	M	L	S	L	L	S	L	L	L	M		
CO4	M	M	L	L	L	S	M	L	M	L		
CO5	L	M	S	M	M	M	L	L	M	M		





Co	urse code	23A	SEMICONDUCTOR DEVICES	L	T	P	C	
	Core/Ele Suppor		Core Paper II:	5			4	
	Pre-req	uisite	Higher secondary physics	Sylla Vers		202 24	3-	
Cou	rse Object	tives:						
			s course are to:					
*			nts to understand and gain the knowledge					
		onductor de	evices. lents with construction, theory and characteristics of the	alaatr	onio			
•	10 acqu	ann the stuc	ients with construction, theory and characteristics of the	electi	OHIC			
Exp	ected Cou	rse Outcon	nes:					
_			etion of the course, student will be able to:					
1	Explain the	he structure	of the basic electronic devices			K	1	
2	Understan	d the charac	cteristics and operations of special diodes			K	2	
3	Understan	d the charac	cteristics and operations of transistors			K	2	
4	Understan	d the charac	cteristics and operations of FET and UJT			K	2	
5	Use the sp	ecial diode	s for various applications			K	3	
K1	- Rememb	per; K2 - Uı	<mark>nd</mark> erstand; K3 - Apply; K4 - Analyse <mark>; K5</mark> - <mark>Evaluate; K</mark>	6 - C1	reate			
Un	it:1		PN JUNCTION DIODE		15	hour	rs	
Extr	insic Semi	cond <mark>uctor –</mark>	od Conduction in Insulator - Semiconductor, Conductor - Doping — P Type — N Type Semiconductor - Formativerse Bias Condition — Characteristics - Clipping and Conductor - Characteristics - Characteristics - Clipping and Conductor - Characteristics - Ch	on of	PN J			
	it:2		SPECIAL DIODES			hou	rs	
Reco	overyDiode	-PointConta	ristics – Breakdown - Backward Diode – Varactor Dio actDiode-ShcottkeyDiode-TunnelDiode-GunnDiode- - PNPN Diode	de - Si	tep			
	it:3	FIN Diode -	BJT	/	151	our		
<u> </u>		o Bipolar J	unction Transistor – Construction - Transistor Biasir	1g - C				
		137	- CB, CE &CC Configuration - Bias Stability - Load	_	-			
	•		ector to Base Bias - Voltage Divider Bias - Bias Compe	ensatio	on -Tl	nerm	nal	
	away - Hea	ntSink	SUIP PET AND THE		151			
	it:4	EET Co	potruction and Operation of N Channel IEET Drai	in Ch		10ur		
			Instruction and Operation of N-Channel JFET - Draid JT - Introduction to MOSFET - Enhancement MOS					
	MOSFET - FET as a Voltage Variable Resistor(VVR) - Introduction to UJT - Characteristics -							
			tor – SCR – TRIAC-DIAC					
Un	it:5		OPTOELECTROIC DEVICES		15	houi	rs	
			Characteristics Of Opto Electronic Devices: LDR – Ph					
			c Cell – Solar Cell – Photo Emissive Sensors – Vac		Photo	Tub	oe-	
Gap	Filled Pho	to Tube – P	Photo Multiplexer – LED – IR Emitter – LCD –Optocou	uplers				
			Total Lecture hours		75	hou	rs	

Tex	t Book(s)
1	S. Salivahanan, N. Suresh Kumar, A. Vallavaraj, "ELECTRONICS DEVICES AND
	CIRCUITS", Tata McGraw Hill Publishing Company Limited, New Delhi, 8th edition.
2	B. L. Theraja, "BASIC ELECTRONICS – SOLID STATE DEVICES", S.Chand&Company
	Ltd. 2000
Ref	erence Books
1	S.L. Kakani, K. C. Bhan Dai—ATEXTBOOKOFELECTRONICS
2	BernardGrob"BASIC ELECTRONICS"-Tata McGraw-Hill Publishing Company Limited,

- Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

 1 https://nptel.ac.in/courses/108/108/108108122/
- 2 https://nptel.ac.in/courses/108/108/108108112/
- 3 https://nptel.ac.in/courses/115/102/115102103/

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes											
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
					The second second			1				
CO1	S	S	L	M	L	M	M	M	S	M		
				115			=3					
CO2	L	L	L	L	L	M	M	M	L	L		
			6	1	To be seen	200						
CO3	L	M	L	L	S	M	L	L	M	M		
					33		// 3					
CO4	L	M	M	L	L	S	M	L	M	S		
									97			
CO5	M	L	L	M	L	M	M	L	M	M		
		5			D 1	MA		15				

^{*}S-Strong; M-Medium; L-Low



SCAADATED:18.06.2024, REVISED JUNE 2024

Course code 33A	DIGITAL PRINCIPLES AND APPLICATIONS	L	Т	P	С
Core/Elective/ Supportive:	Core Paper III	3			4
Pre-requisite:	Basic Physics	Sylla Vers		2023	3-24

Course Objectives:

The main objectives of this course are to:

- ❖ To acquire the basic knowledge of Number system, Digital logic circuits and its application
- ❖ To outline the formal procedures for the analysis and design of combinational and sequential circuits
- ❖ To learn the concepts of A/D, D/A conversions and their types

Expected Course Outcomes

On the successful completion of the course, student will be able to:

1	Understand the basics of Number system and gates	K2
2	Realize the operation of various logic gates and analyzing the outputs	K1
3	Analyze and design the combinational logic circuits	K4
4	Analyze and design the Sequential logic circuits	K4
5	Design various synchronous and asynchronous sequential circuits	K6

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

Unit:1 NUMBER SYSTEMANDCODES 12hours

Decimal, Binary, Octal and Hexa Decimal Numbers – Conversion — Binary Addition, Subtraction and Multiplication – 1's and 2's Compliments - Binary Coded Decimal (BCD) – Weighted Codes and Non-weighted Codes – Excess Three – Grey Code.

Unit:2 BOOLEAN ALGEBRA AND LOGIC GATES 12hours

Boolean logic operations – Boolean functions – Truth Tables – Basic Laws – DeMorgans Theorem – Sum of Products and Products of Sums – Karnaugh map –Universal Gates – Logic Gates – OR, AND, NOT, NAND, NOR, EX-OR and EX-NOR Gates .

Unit:3 COMBINATIONALLOGICCIRCUITS 12hours

Half Adder – Full Adder – Half Sub tractor – Full Sub tractor – 4 bit Binary Adder / Subtractor – BCD adder – Multiplexer – Demultiplexer – Decoders – Encoders – Parity Generators / Checkers .

Unit:4 SEQUENTIALLOGICCIRCUITS 12hours

Flip Flops – RS, Clocked RS, JK, JK Master Slave, D and T Flip Flops – Shift Registers and its Types – Ring Counters – Ripple Counters – Synchronous Counter – Up Down counter – Decade Counter – Applications

Unit:5	D/A AND A/D CONVERTERS	12 hours
Digital to	Analog Converters: Resistive Divider Type - Ladder Type – Accuracy and Res	solution -
	o Digital Converters: Counter-simultaneous Conversion - Dual Slope Type - A	Accuracy
and Reso	plution.	
	Total Lecture hours	60
		hours
Text Bo	ok(s)	
1	Malvino & Leech, — Digital Principles and Applications ", Tata McGrawHill Edi V, 2002.	tion
2	M.Morris Mano , Digital Logic and Computer Design", PHI 2005.	
	Reference Books	
1	Floyd and Jain, Digital Fundamentals , Prentice Hall2010	
	M. Morris Mano Charles Kime, Digital Logic and Computer Design Fundan	nentals,
2	Pearson Education Limited, 2014	
Related	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	
1	https://soaneemrana.org/onewebmedia/DIGITAL%20PRINCIPLES%20AND%	20APPL
	ICATION% 20BY% 20LEACH% 20&% 20MALVINO.pdfE book, Malvino & 1	
	—DIGITALPRINCIPLESANDAPPLICATIONS, TataMcGrawHillEditionXI,20	11
2	https://nptel.ac.in/courses/117/106/117106086/Introduction to digital circuits	
	Course Designed By: Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tir	upur &
	S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.	

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Mappi	ng with P	rogra <mark>mn</mark>	ne Outc	omes			47.5		AS 1	
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	So	S	M	M	M	S	GE,	L	S
CO2	S	S	S	9 <u>M</u>	M	M of 2_U	IT S	L	L	L
CO3	S	S	S	M	ATE TO	ELHAT	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course Code	33B	ELECTRONIC CIRCUITS	L	T	P	C
Core/Elective/ Supportive:	Core pap	er IV	3			4
Pre-Requisite:		Basic Physics		abus sion	2023-	24

Course Objectives:

The Main Objectives of this course are to:

- ❖ To enable the students to understand and gain the knowledge on power supplies, amplifiers and oscillators.
- ❖ To acquaint the students with construction, theory and characteristics of the electronic amplifier circuits and types of multivibrators.

Expected Course Outcomes:

On the successful completion of the	course, student will be able to:
-------------------------------------	----------------------------------

1	understand the concepts of Rectifiers and regulators	K2
2	Study about Small signal amplifiers	K1
3	analyse the functions of power amplifiers	K4
4	analyse the performance of negative as well as positive feedback circuits	K4
5	design oscillators and Multivibrators	K6

K1:Remember; K2-Understand; K3-Apply; K4-Analyze; K5-Evaluate; K6-Create
Unit:1 RECTIFIERS AND REGULATORS 12 hours

Half wave, Full waves and bridge Rectifiers – Calculation of RMS Value – Average Value – Ripple Factor – Efficiency – Transformer Utility Factor – Peak Inverse Voltage – Inductor Filter – Capacitor Filter – LC Filter - Voltage Doubler – Voltage Regulator – Construction of DC Power Supply.

Unit:2 SMALL SIGNAL AMPLIFIERS 12 hours

CE, CB, CC amplifiers—Current Gain - Voltage Gain — Power Gain — Single Stage Transistor Amplifier — DC and AC load line — RC Coupled Amplifier —Transformer Coupled Amplifier.

Unit:3 POWER AMPLIFIERS 12 hours

Operation and Graphical Representation of Class A, Class B, Class C and Class AB Amplifiers – Maximum Collector Efficiency of Class A Power Amplifier – Harmonic Distortion – Class B Push Pull Amplifier .

Unit:4	FEEDBACK AMPLIFIERS	12
		hours

Basic concepts of feedback – Positive Feedback – Negative Feedback – Effects of Negative Feedback on Gain, Bandwidth and Distortion – Noise – Voltage Series Feedback - Voltage Shunt Feedback – Current Series Feedback – Current Shunt Feedback.

Unit:5	OSCILLATORS AND MULTIVIBRATORS	12 hours
Barkh	nausen Criterion – Hartley Oscillator – Colpitts Oscillator – Phase Shift Osc	cillator –
Wein Bi Trigger	ridge Oscillators - Astable Multivibrator - Monostable multivibrator -	Schmitt
	Total Lecture hours	60 hours
Text Bo	ok(s)	
1	S.K.Sahdev, Electronic Principles", Dhanpat Rai & Co(P) Ltd, 2nd Edition, 19	98
2	B.L.Theraja, "Basic Electronics", Chand Company Ltd, 2000.	
Referen	ce Books	
1	V.K.Metha, Rohit Metha, —Principles of Electronics" S Chand, 2006.	
2	B.Sasikala, C.Poornachandra, Electronic Devices and Circuits", Scitech 2	2003.
Related	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	
1	http://www.ee.iitm.ac.in/~ani/2012/ec5135/lectures.htmlLecture Notes	
2	https://nptel.ac.in/courses/108/102/108102097/#Introduction to Electronic on NPTEL.	circuits
3	https://nptel.ac.in/courses/108/102/108102095/Analog Electronic circuits N	IPTEL.
	Course Designed By:	
Г	Dr.K.Ven <mark>mathi ,As</mark> sistant pro <mark>fessor,L</mark> .R.G.Go <mark>vt Art</mark> s C <mark>olle</mark> ge <mark>for Wom</mark> en, Ti	rupur &
	Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Ero	de.

Mappi	ng with P	rogra <mark>mn</mark>	ne Outc	omes	2	EAL	1			A
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M5 EDU	M ₁₆₀	OUT BULL FI EVAT	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

	3AD OBJECT ORIENTED I PROGRAMMING USING C++	T	P	C
Core/Elective/	ALLIED PAPER III 3	3		3
Supportive:			2021	
Pre-Requisite:	V	yllabus Tersion		3-24
Course Objectives:				
	ves of this course are to;	1		
5	ct deals with the programming concepts of Object Oriented ing using C++.	1		
	a skills on using <mark>MS word, MS Excel, Powe</mark> r Point andMS	SAcces	c	
Expected Course C			:	
	com <mark>pletion</mark> of th <mark>is subject the students should ha</mark> ve :Writin nc <mark>epts like Encap</mark> sulation, Data abstraction, Inheritance, P			
Exception handling	•	Olymo	трипѕи	i and
1	te and understand Basic of Computer			K2
	Practice of Word Processing			K3
	Practice of MS Excel			К3
4 Knowled	lge to Make Small Presentation			K6
5 Use and 1	Practice of MS Access			K3
K1:Remember;	K2-U nderstand; K3 -Apply; K4 -Analyze; K5 -Evaluate	e; I	K6 -Cre	ate
Unit:1			9 h	ours
GOTO Switch Cas	se Statements			
Unit:2	Se Statements		9 h	ours
Unit:2 Class and Objects: I variables and functi	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions – Characteristics – Calling constructor and Destructors	ember		ours
Unit:2 Class and Objects: I variables and functi	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions	ember		ours
Unit:2 Class and Objects: I variables and functi Bit fields and Class Unit:3	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions – Characteristics – Calling constructor and Destructors	3		
Unit:2 Class and Objects: I variables and functi Bit fields and Class Unit:3 Operator Overloadin functions – Type co	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions	iend	9 h	ours
Class and Objects: I variables and functi Bit fields and Class Unit:3 Operator Overloading functions — Type co Hierarchical, Hybronic Service (1988)	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions – Characteristics – Calling constructor and Destructors ng: Overloading unary, Binary operators – Overloading fronversion - Inheritance: Types of inheritance: Single, Mult	iend	9 h	ours ble,
Unit:2 Class and Objects: I variables and functi Bit fields and Class Unit:3 Operator Overloadin functions – Type co	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions – Characteristics – Calling constructor and Destructors ng: Overloading unary, Binary operators – Overloading fronversion - Inheritance: Types of inheritance: Single, Mult	iend	9 h	ours
Class and Objects: I variables and functi Bit fields and Class Unit:3 Operator Overloadin functions – Type co Hierarchical, Hybrotherarchical, Hybrotherator Classes and base classes and base classes.	Declaring objects – Defining member functions – Static mons – Array of objects – Overloading member functions – Characteristics – Calling constructor and Destructors ng: Overloading unary, Binary operators – Overloading fronversion - Inheritance: Types of inheritance: Single, Mult	iend ilevel,	9 h	ours ours ved

Files: File stream classes – File Modes – Sequential read/write operations - Random access operation – Templates – Exception handling – Strings – Miscellaneous functions

	Total Lecture hours	45 hours					
Text Bo	ook(s)						
1 Ashok N Kamthane: Object Oriented Programming with ANSI and Turbo C++,							
	Pearson						
	Education Publ., 2003.						
Refere	nce Books						
1	E. Balagurusamy: Object Oriented Programming with C++, TMH Pub.,	1998.					
2	Maria Litvin and Gary Litvin: C++ for you++, Vikas Publ, 2002						
3	John R Hubbard: Programming with C++, TMH Publ. II Edition, 2002						
	Course Designed Prus						
	Course Designed By: Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women,	TT' 0					

Or.K.Venmathi <mark>,Assi</mark>stant professor,L.R.G.Govt A<mark>rts Co</mark>lleg<mark>e for Wom</mark>en, Tirupur & Mr<mark>.S.Shanka</mark>r , Assistant Professor, Sri Vasavi <mark>Col</mark>leg<mark>e, SF Win</mark>g, Erode.

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Mappi	ng with P	<mark>rogr</mark> amn	ne Outc	omes	-					
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
			1 m	1000	con As			30		
CO1	S	S	M	M	M	M	S	M	M	S
		Σ:		5	The same		100			n /
CO2	S	M	M	M	M	M	S	L	AL.	L
		2	100				193		6	7
CO3	S	C S	S	M	M	L	L	L	M	M
		000		Ĭ				000	7	
CO4	M	M	M	S	S	S	L	L	M	M
			15B1				· 81			
CO5	M	M	S	S	M	L	M	M	S	M
				File	JUI 60)) =				
					AIETU	313		•	•	•

^{*}S-Strong; M-Medium; L-L

SCAADATED:18.06.2024 ,REVISED JUNE 2024

	Course code 3ZA BIOMEDICAL INSTRUMENTATION L T							
	Core /Elective / Skill Based Subject: I 3 Supportive:							
Pre-requisite Higher secondary biology Syllabus Version								
Course Object			•	•				
To prese	nts various	is course are to: s bio-potentials and working principles ofmedical instruents to learn about bio-potentials andmedical instrumen						
Expected Cou	rse Outco	mes:						
		etion of th <mark>e course, stude</mark> nt will be able to:						
1 Understan	d the Conc	cept of bio-potential			K	2		
2 Understan	d the conc	ept of medical instruments			K	2		
3 Develop th	ne trou <mark>bles</mark>	hooting Skills of medical instruments			K	3		
4 Understar	nd the cond	cepts of signal conditioners & diagnostic equipment			K	2		
5 Analyse pl	nysi <mark>ologic</mark>	al assist devices			K	4		
Unit:1	, , , , , , , , , , , , , , , , , , ,	Inderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate; BASIC PHYSIOLOGY			our	<u> </u>		
Transmembran Unit:2	e Potential	- Nervous System - Physiology of Muscles - Heart and ELECTRODES AND TRANSDUCERS	d Blood		ulati 10ur			
	e Theory	- Micro Electrodes - Skin Surface Electrodes - N	Jeedle					
Equivalent Cir	<mark>cuit - El</mark> ec	etrode Materials - Chemical Electrodes - Reference E	N. Contraction	10				
Electrode - Blo								
Unit:3	SIC	GNAL CONDITIONERS & DIAGNOSTIC EQUIPMENTS		9 h	ours	}		
Unit:3 Instrumentation Pass, High Pass	n Amplifies and Band	rs - Current Amplifiers - Isolation Amplifier - Need for Pass Active Filters - Notch Filters - Heated Stylus and EQUIPMENTS: Typical Electrocardiogram (ECG)-F	l Ink P	s - Lo en	W			
Unit:3 Instrumentation Pass, High Pass Recorders. DIA	n Amplifies and Band AGN <mark>OSTIO</mark> nalogram (rs - Current Amplifiers - Isolation Amplifier - Need for Pass Active Filters - Notch Filters - Heated Stylus and EQUIPMENTS: Typical Electrocardiogram (ECG)-F	l Ink P	s - Lo en cardio	W	h		

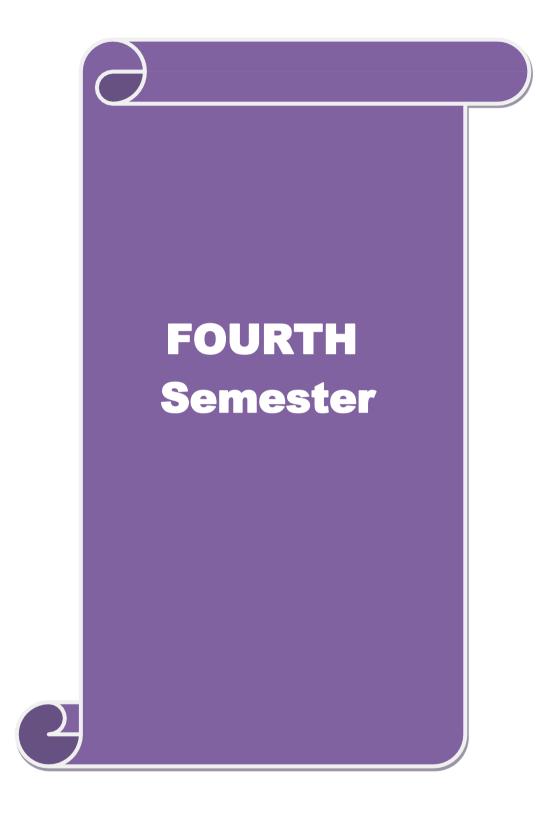
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Unit:5	PHYSIOLOGICAL ASSIST DEVICES	9 hours
Need for Pace	makers - Pacemaker Parameters and Circuits - Different Mode	es of Operation - DC
Defibrillator	- Heart Lung Machines - Nerve and Muscle Stimu	lator. COMPUTER
APPLICATIO	NS: Analysis of ECG signals .	
	Total Lecture hours	45 hours
Text Book(s)		
	Carr and John M. Brown, "Introduction to Biomedical Equip	ment
Technol	Ot 7	
	Cromwell.,FredJ. Webell., Erich A. Pfeffer.,"Bio-me	edical
Instrum	entation	
D.C. D.	- 1000 mm 2007	
Reference Bo		
1 Khandpu New	r, "Han <mark>dbook on Biomedical Instrumentation",</mark> Tata McGrav	v Hill Company,
	Vebster, Ed., "Medical Instrumentation Application and Designment & Sons, Singapore, 1999	n", Third Edition,
3 Arumuga	m.M <mark>,"BiomedicalInstrumentation",</mark> AnuradhaAgenciesPublishe	ers,Chennai,1992
	461	x
1		
Related Onli	ne Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1 https://ww	w.youtube.com/watch?v=i2mZylgP1Fk	
2 https://ww	w.youtube.com/watch?v=4ldv98F7Zng	
	el.ac.in/courses/108/105/108105101/	
4 https://npt	el.ac.in/courses/108/105/108105091/	3
	Course Designed By:	9
	Venmathi ,Ass <mark>istant professor,L.R.G.Govt Arts College</mark> for Wor	-

Dr.K. Venmathi, Assistant profess	or,L.R.G.Govt A	rts College for Wo	omen, Tirupur &
Mr.S.Shankar, Assistant Profe	essor, <mark>Sri Vasavi</mark> (College, SF Wing.	, Erode.
	Colmbatore 1	G	

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	L	L	M	M	M	L	L	M	S	M
CO2	L	M	M	L	L	M	L	S	L	S
CO3	M	L	S	L	M	L	L	M	M	L
CO4	L	L	S	M	S	S	L	S	M	L
CO5	L	L	S	S	M	L	S	L	M	S

^{*}S-Strong; M-Medium; L-Low



Course Objectives: To impart the knowledgeon IC fabrication, Timer, PLL, and electronic instruments To enable the students to acquire the knowledge of Op-amp., transducers and its applications in electronic circuits and know the technique of measurements using electronic instruments Expected Course Outcomes: On the successful completion of the course, student will be able to: Recognize the standardsin IC Fabrication Technology Understand the workingofTimer and PLL Design simplecircuits usingOp-amp. Understand the principle ofvarioustypes of transducers and working of frequently used equipment slike CRO Digital Voltmeter etc. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1 ICFABRICATIONTECHNOLOGY 12 Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation EpitaxialGrowth-Oxidation-Photolithography-DiffusionofImpurities—Isolation Technique Metallization - Monolithic Transistors—IntegratedResistors- Thin and Thick Film Technolounities Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer - Monostable Operation - Applications: Chimit Trigger - FSK Generator - Phase Locked Loop: Functional Block Diagram - Phase Detector / Comparator - Voltage Controlled Oscillator - Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier - Op-amp Parameters - Summing Amplifier - Difference Amplifier - Integrator - Differentiator - Instrumentation Amplifier - Voltage to Current Converter - Current to Voltage Converter - Phase Shift Oscillator - Wein Bridge Oscillator-Square Wave and Triangular Wave Generator	C		
Pre-requisite Basic Electronics Syllabus Version 202. Course Objectives:	4		
❖ To impart the knowledgeon IC electronic instruments fabrication, Timer, PLL, and electronic instruments ❖ To enable the students to acquire the knowledge of Op-amp., transducers and its applications in electronic circuits and know the technique of measurements using electronic instruments Expected Course Outcomes: On the successful completion of the course, student will be able to: 1 Recognize the standardsin IC Fabrication Technology 1 2 Understand the workingofTimer and PLL 1 3 Design simplecircuits usingOp-amp. 1 4 Understand the principle ofvarioustypes of transducers and working of frequently used equipment'slike CRO K2 Study the construction and working of frequently used equipment'slike CRO 5 Digital Voltmeter etc. 1 K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create 1 Unit:1 ICFABRICATIONTECHNOLOGY 1 Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation EpitaxialGrowth-Oxidation-Photolithography-DiffusionofImpurities-Isolation Technique Metallization-Monolithic Transistors-IntegratedResistors- Thin and Thick Film Technolound Diffusion Plant P	2023-24		
electronic instruments To enable the students to acquire the knowledge of Op-amp., transducers and its applications in electronic circuits and know the technique of measurements using electronic instruments Expected Course Outcomes: On the successful completion of the course, student will be able to: Recognize the standardsin IC Fabrication Technology Understand the workingofTimer and PLL Design simplecircuits usingOp-amp. Understand the principle ofvarioustypes of transducers and working of frequently used equipment'slike CRO Digital Voltmeter etc. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1			
On the successful completion of the course, student will be able to: 1 Recognize the standardsin IC Fabrication Technology 2 Understand the workingofTimer and PLL 3 Design simplecircuits usingOp-amp. 4 Understand the principle ofvarioustypes of transducers and working of frequently used equipment'slike CRO 5 Digital Voltmeter etc. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1 ICFABRICATIONTECHNOLOGY 12 Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation EpitaxialGrowth-Oxidation-Photolithography-DiffusionofImpurities-Isolation Technique Metallization-MonolithicTransistors-IntegratedResistors- Thin and Thick Film Technolounit:2 TIMERANDPLL Functional Block Diagram of 555 timer - Monostable Operation - Applications: Schmitt Trigger - FSK Generator - Phase Locked Loop: Functional Block Diagram - Phase Detector / Comparator - Voltage Controlled Oscillator - Low Pass Filter. Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier - Op-amp Parameters - Summing Amplifier - Difference Amplifier - Integrator - Differentiator - Instrumentation Amplifier - Voltage to Current Converter - Current to Voltage Converter - Phase Shift Oscillator - Wein Bridge Oscillator-Square Wave and Triangular Wave Generator			
Recognize the standardsin IC Fabrication Technology			
2 Understand the workingof Timer and PLL 3 Design simplecircuits using Op-amp. 4 Understand the principle of various types of transducers and working of frequently used equipment's like CRO 5 Digital Voltmeter etc. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1 ICFABRICATIONTECHNOLOGY 12 Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation Epitaxial Growth - Oxidation - Photolithography - Diffusion of Impurities - Isolation Technique Metallization - Monolithic Transistors - Integrated Resistors - Thin and Thick Film Technolog Unit:2 TIMERANDPLL 12 Functional Block Diagram of 555 timer - Monostable Operation - Applications: Schmitt Trigger - FSK Generator - Phase Locked Loop: Functional Block Diagram - Phase Detector / Comparator - Voltage Controlled Oscillator - Low Pass Filter. Unit:3 OPERATIONAL AMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier - Op-amp Parameters - Summing Amplifier - Difference Amplifier - Integrator - Differentiator - Instrumentation Amplifier - Voltage to Current Converter - Current to Voltage Converter - Phase Shift Oscillator - Wein Bridge Oscillator - Square Wave and Triangular Wave Generator			
3 Design simplecircuits usingOp-amp. 4 Understand the principle ofvarioustypes of transducers and working of frequently used equipment'slike CRO 5 Digital Voltmeter etc. K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1 ICFABRICATIONTECHNOLOGY Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation EpitaxialGrowth-Oxidation-Photolithography-DiffusionofImpurities-Isolation Techniqu Metallization-Monolithic Transistors-IntegratedResistors- Thin and Thick Film Technolo Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer - Monostable Operation - Applications: - Linear Ramp Generator - Pulse Width Modulator - Astable Operation - Applications: Schmitt Trigger - FSK Generator - Phase Locked Loop: Functional Block Diagram - Phase Detector / Comparator - Voltage Controlled Oscillator - Low Pass Filter. Unit:3 OPERATIONALAMPLIFIER Ideal Characteristics - Inverting and Non-inverting Amplifier - Op-amp Parameters - Summing Amplifier - Difference Amplifier - Integrator - Differentiator - Instrumentation Amplifier - Voltage to Current Converter - Current to Voltage Converter - Phase Shift Oscillator - Wein Bridge Oscillator-Square Wave and Triangular Wave Generator	K1		
Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer – Monostable Operation – Applications: — Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Unit:3 OPERATIONALAMPLIFIER 12 Integrator – Op-amp Parameters – Summing Amplifier – Opfigenerator – Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator – Square Wave and Triangular Wave Generator – Square Wave and Triangular Wave Generator – Square Vave and Triangular Wave Generator – Square Vave and Triangular Wave Generator – Square Vave and Triangular Vave Generator – Square Vave – Squ	K2		
and working of frequently used equipment's like CRO Digital Voltmeter etc.	K3		
K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create Unit:1 ICFABRICATIONTECHNOLOGY 12 Fundamentals of Monolithic IC Technology - Basic Planar Process- Wafer Preparation EpitaxialGrowth-Oxidation-Photolithography-DiffusionofImpurities-Isolation Techniqu Metallization-MonolithicTransistors-IntegratedResistors- Thin and Thick Film Technolo Unit:2 TIMERANDPLL 12 Functional Block Diagram of 555 timer - Monostable Operation - Applications: - Linear Ramp Generator - Pulse Width Modulator - Astable Operation - Applications: Schmitt Trigger - FSK Generator - Phase Locked Loop: Functional Block Diagram - Phase Detector / Comparator - Voltage Controlled Oscillator - Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier - Op-amp Parameters - Summing Amplifier - Difference Amplifier - Integrator - Differentiator - Instrumentation Amplifier - Voltage to Current Converter - Current to Voltage Converter - Phase Shift Oscillator - Wein Bridge Oscillator-Square Wave and Triangular Wave Generator	K4		
Fundamentals of Monolithic IC Technology – Basic Planar Process- Wafer Preparation EpitaxialGrowth–Oxidation–Photolithography–DiffusionofImpurities—Isolation Techniqu Metallization–MonolithicTransistors—IntegratedResistors- Thin and Thick Film Technolo Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer – Monostable Operation – Applications: – Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER I2 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator	K5		
Fundamentals of Monolithic IC Technology – Basic Planar Process- Wafer Preparation EpitaxialGrowth–Oxidation–Photolithography–DiffusionofImpurities—Isolation Techniqu Metallization–MonolithicTransistors—IntegratedResistors- Thin and Thick Film Technolo Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer – Monostable Operation – Applications: – Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator			
EpitaxialGrowth–Oxidation–Photolithography–DiffusionofImpurities–Isolation Technique Metallization–MonolithicTransistors–IntegratedResistors- Thin and Thick Film Technology Unit:2	hours		
Metallization–MonolithicTransistors–IntegratedResistors- Thin and Thick Film Technolo Unit:2 TIMERANDPLL Functional Block Diagram of 555 timer – Monostable Operation – Applications: – Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator			
TIMERANDPLL Functional Block Diagram of 555 timer – Monostable Operation – Applications: – Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator	ies-		
Functional Block Diagram of 555 timer – Monostable Operation – Applications: – Linear Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator	ogy.		
Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator	Chours		
Ramp Generator – Pulse Width Modulator – Astable Operation – Applications: Schmitt Trigger – FSK Generator - Phase Locked Loop: Functional Block Diagram – Phase Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator			
Detector / Comparator – Voltage Controlled Oscillator – Low Pass Filter . Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier – Op-amp Parameters – Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator – Square Wave and Triangular Wave Generator			
Unit:3 OPERATIONALAMPLIFIER 12 Ideal Characteristics - Inverting and Non-inverting Amplifier — Op-amp Parameters — Summing Amplifier — Difference Amplifier — Integrator — Differentiator — Instrumentation Amplifier — Voltage to Current Converter — Current to Voltage Converter — Phase Shift Oscillator — Wein Bridge Oscillator—Square Wave and Triangular Wave Generator			
Ideal Characteristics - Inverting and Non-inverting Amplifier — Op-amp Parameters — Summing Amplifier — Difference Amplifier — Integrator — Differentiator — Instrumentation Amplifier — Voltage to Current Converter — Current to Voltage Converter — Phase Shift Oscillator — Wein Bridge Oscillator—Square Wave and Triangular Wave Generator			
Summing Amplifier – Difference Amplifier – Integrator – Differentiator – Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator	hours		
Instrumentation Amplifier – Voltage to Current Converter – Current to Voltage Converter – Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator			
– Phase Shift Oscillator – Wein Bridge Oscillator–Square Wave and Triangular Wave Generator			
	r		
Unit:4 TRANSDUCERS 12	hours		
Introduction – Electrical Transducer – Basic requirements of Transducer – Classification	iioui 5		
of Transducers – Selection of Transducers – Resistive Transducers – Potentiometers –			
Thermistors – Thermocouple – Piezoelectric Transducers – Hall Effect Transducers –			
Photoelectric Transducers – Digital Displacement Transducers.			

Unit:5		12						
	ELECTRONIC INSTRUMENTS	hours						
Q Meters- CRO: Block Diagram - Cathode Ray Tube - Measurement of Frequency -								
Measure	ment of Voltage and Current - Digital Oscilloscope - Digital voltmeter- I	Dual						
Slope In	ntegrating Type DVM - Digital Multimeter - Humidity Measuremen	t –						
Measure	ment of PH.							
	Total Lecture hours	60						
		hours						
Text Boo	$\mathbf{k}(\mathbf{s})$							
1	D.Roy Choudhury and Shahil B Jain, Linear Integrated Circuits", Second Edi	tion						
	New Age International Publishers, 2004							
2	K.R.Botkar,—Integrated Circuits", 10th Edition KhannaPublishers, 2006							
Reference	ee Books							
	J.B.GUPTA, A Course In Electronic and Electrical Measurements and							
1	Instrumentation", 12th Edition, S.K Kataria & Sons.							
2	A.K. Sawhney, ELECTRICAL & ELECTRONIC MEASUREMENTS AN	D						
	INSTRUMENTATION:, Dhanpath Rai & Co (P) Ltd, 2004.							
Related	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	197						
.1	https://nptel.ac.in/courses/108/108/108108111/Integrated circuits,opamps and the	neir						
	applicati <mark>ons</mark>							
2	https://nptel.ac.in/courses/117/106/117106030/Analog IC Design	1						
	Course Designed By:	1						
D	r.K.Venmathi <mark>,Assistant professor,L.R.G.Govt Arts College for</mark> Women, Tirupu	r &						
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode							

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	SSIL	M	M	S
CO2	S	M	M	M	ATE TO	M MELEVAT	S	L	L	L
CO3	S	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

SCAADATED:18.06.2024 ,REVISED JUNE 2024

Course code 4AD	PYTHON PROGRAMMING	L	T	P	C
Core/Elective/ Supportive	ALLIED PAPER IV	3	0	0	4
Pre-requisite	Knowledge on logic of the programs and oops concept.	Syllab Versio		2023	-24

CourseObjectives:

The main objectives of this courseareto:

- 1. To introduce the fundamentals of Python Programming.
- 2. To teach about the concept of Functions in Python.
- 3. To impart the knowledge of Lists, Tuples, Files and Directories.
- 4. To learn about dictionaries in python.
- 5. To explore the object-oriented programming, Graphical programming aspects of python with help of built in modules.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1	Remembering the concept of operators, data types, looping statements in Python	K1
	programming.	
2	Understanding the concepts of Input / Output operations in file.	K2
3	Applying the concept of functions and exception handling	К3
4	Analyzing the structures of list, tuples and maintaining dictionaries	K4
5	Demonstrate significant experience with python program development environment	K4-K6

K1-Remember; K2-Understand; K3-Apply; K4-Analyze; K5-Evaluate; K6-Create

Unit:1 BASICS OF PYTHON 10hours

BASICS: Python - Variables - Executing Python from the Command Line - Editing Python Files - Python Reserved Words - Basic Syntax-Comments - Standard Data Types - Relational Operators - Logical Operators - Bit Wise Operators-Simple Input and Output.

Unit:2 CONTROL STATEMENTS 10hours

CONTROL STATEMENTS: Control Flow and Syntax - Indenting - if Statement - statements and expressions - string operations - Boolean Expressions - while Loop - break and continue - for Loop.LISTS:List-listslices-listmethods-listloop—mutability—aliasing-cloninglists-

listparameters. TUPLES: Tupleassignment, tupleasreturn value-Sets-Dictionaries

Unit:3 FUNCTIONS 10hours

FUNCTIONS: Definition - Passing parameters to a Function - Built-in functions- Variable Number of Arguments. **Libraries in Python**: Tensor Flow, Matplotlib, Open CV, Pandas, Num Py, SciPy, Scrapy, PyTorch,

Scikit Learn.

Unit:4 ERROR HANDLING 12hours

ERROR HANDLING: Run Time Errors - Exception Model - Exception Hierarchy - Handling Multiple Exceptions-Handling IO Exceptions .**DataStreams**-AccessModesWriting-Data to a File Reading- Data From a File - File Methods – Working with Directories.

U	nit:5	OBJECT ORIENTED FEATURES	10hours
OB	JECT OR	IENTED FEATURES: Classes Principles of Object Orientation	- Creating Classes –
		ods-Class Variables–Inheri <mark>tance – Polymo</mark> rphism <mark>- R</mark> egular Expr	
		~\巫从!	
	nit:6	ContemporaryIssues	3hours
E	xpert lecture	es,online sem <mark>inars -webinars</mark>	
		100	
		TotalLecturehours TotalLecturehours	55hours
To	extBook(s)		
1		nmerfield, Programming in Python3: A Complete introduction to the Addison-Wesley Professional, 2009.	ne Python
2		Brown, P <mark>YTHON</mark> : The Complete Reference <mark>, McG</mark> raw <mark>-Hill, 2</mark> 001	
3	E. Balagu Edition.	rusamy (2 <mark>017), "Problem Solving and Python Programming", Mo</mark>	cGraw-Hill, First
	•		
R	eference Bo	ooks	
1		own <mark>ey,"Think</mark> Python: How to Think Like a Comp <mark>ute</mark> r Scientist" a 3, Shroff /O' Reilly Publishers, 2016	² , 2 nd edition, Updated
2	The state of the s	Ross <mark>um and Fred L. Drake Jr, An Introduction to Python – Rev</mark> 3.2, NetworkTheory Ltd., 2011	ised and updated
3	Wesley J	Chun, Core Python Applications Programming, Prentice Hall, 2012	2.
		A.43.	
R	elated Onli	ne Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
1	Python for	Everybody-Specialisation - Coursera	
2	Learn Pyth	on: The Complete Python Programming Course - Udemy	
3	Python for	Data Science – NPTEL / Swayam	发
		2	9
Co	ourse Desig	ned By:	
		r.K.Venmathi , <mark>Assistant professor,L.R.G.Govt Arts Co</mark> llege for V	•
		Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Win	ig, Erode.
		9,6	
		Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Win	ig, Erode.



Mapping with Programme Outcomes										
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S	S	S	L	S	M	L	M	S	S
CO2	S	S	S	L	S	M	L	M	S	S
CO3	S	S	S	L	S	M	L	M	S	S
CO4	S	S	S	L	S	M	L	M	S	S
CO5	S	S	S	L	S	M	L	M	S	S



			DIGITAL ELECTRONICS AND							
Cour	rse Code	43P	LINEAR IC's LAB	L	T	P	C			
	re/Elective/ Supportive: Core Practical III						3			
	equisite:	Digital	Electronics and Microprocessor theory	SvII	abus	us 2023-24				
	equisitet	2191441	220010 onies una milieroprocessor uncorg		sion	-020				
Course	Objectives:					•				
The Ma	in Objectiv	es of this	course are to:							
			ical operation of various gatesand theorems	S						
			gital circuits				•.			
			oly language programming of Microproces	sor an	d to ir	iterfac	e it			
V	vith various	periphera	al devices							
					2					
_	d Course C				밀					
			of the course, student will be able to:	2	衏.					
			ing Boolean laws	-	10	K6				
			subtractor circuit using logic gate			K6				
		_	thmetic Programming of 8085			K6				
			c Programming of 8085			K6				
			for <mark>m generation</mark>			K1				
	nember;	K2-Unde	rsta <mark>nd; K3-Apply; K4-Analyze; K5-Ev</mark>	aluate		K6-Cre				
Part I		1 8	DIGITAL		4	40 Ho	ırs			
	1 7	I .C. 1	ELECTRONICS(ANY 6)		Co.					
			on of Basic Gates and Universal gates	81						
			n ofDemorgan's Theorem and Full Adder & Half Subtractor andFull	Subtr	notor					
			unter & BCD Counter	Subu	actor					
		tudy ofFli			6					
			andDe-Multiplexers	-	66					
			d Decoder							
			egmentDisplay							
			ey code and Grey to Binary code 10.							
			nd Asynchronous Counter							
			Simulation of Logic Gate using VHDL Cod	ling						
			Simulation of Adder Circuits using VHDL		g					
Part II		I	LINEAR IC'S LAB			45 o	urs			
			(ANY 6)							
			fultivibratorusing555							
			e Multivibratorusing 555							
		_	nd Non Inverting Amplifier							
			t Oscillator							
			tationAmplifier Current Converter and Current to Voltage (Conve	rtor					
	6 V	onage to	Current Converter and Current to Voltage	Conve	TOI					

- 7 Wein Bridge Oscillator usingIC741
- 8 Square Wave and Triangular Generator
- 9. Adder and Subtractor usingIC741

Total Lecture hours	85
	hours

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mapping wit	h Progran	nme Out	comes					1 3		
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

*S-Strong; M-Medium; L-Low

EDUCATE TO ELEVATE

Course Code	43Q	ELECTRONIC CIRCUITS & COMMUNICATION LAB	L	T	P	C
		COMMUNICATION LAB				
Core/Elective/		Core Practical IV			2	3
Supportive:						
Pre-Requisite:	Electroni	c circuits theory and Communication			2023	24
		theory	Ver	sion		
Course Objective	es:					
The Main Object	tives of this o	course are to:				
 To underst 	tand the conc	ept of working of regulated power suppl	ies,			
rectifiers,	amplifiersand	doscillators.				
 To experir 	nent the mod	lulation anddetectiontechniques.				
F 4 1 C	0.4					
Expected Course		6.4				
		of the course, student will be able to:				TZC
		and rectifier circuits				K6
	mplifiercircu					K6
		llator circuits				K6
		ulation circuits				K6
	communicat					K1
K1 :Remember;	K2-Under	stand; K3 -Apply; K4-Analyze ; K5 -E	valuate	K	6-Cre	
Part I		ELECTRONIC CIRCUITS			45 l	nours
	_	Power Supply using ZenerDiode				
	<mark>oltage</mark> Double					
	<mark>eedba</mark> ckAmp					
	mitterFollow	7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7 7			1	
A CONTRACTOR OF THE CONTRACTOR		Co <mark>upledA</mark> mplifier				
	artley Oscilla					
	olpitts Oscill					
	hase shiftOsc	/ / / / / / / / / / / / / / / / /				
	/ein BridgeO		-			
	RCCoupledA					
The second secon	Hall wave al FilterCircuits	nd Full WaveRectifier	0			
12 1	rmerencuits					
			A 5591			
C.	E.	I ECTRONIC COMMUNICATION		1		40
Part II	E	LECTRONIC COMMUNICATION	367			40 hours
	E ace of IFAmple	AKAR UNI	310			
1. Performan		lifier Collections	301			
1. Performan 2. AM Modu	ce ofIFAmpl	lifier etection				

- 5. PINDiodeOscillator
- 6. Alignment of Dish Antenna
- 7. Alignment of Satellite Receiver
- 8. PWMModulation
- 9. PPMModulation
- 10. PCMModulation
- 11. GUNNDiodeOscillator

(Any 16 Experiments)

Total Lecture hours 85 hours

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	ng with Pi	rogramn	ne Outc	omes	O CANCEL	54 G5//				
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	S	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	M	L	M	M	S	M

*S-Strong; M-Medium; L-Low

இத்தப்பாரை உயர்த்தி EDUCATE TO ELEVATE SCAADATED:18.06.2024, REVISED JUNE 2024

Course code	43R	ALLIED LAB: COMPUTER PROGRAMMING LAB	L	Т	P	С
		OBJECT ORIENTED PROGRAMMING USING C++ & PYTHON PROGRAMMING LAB				
Core/Elective/Supportive		Allied Lab V			2	2
Pre-requisite		Basics of any Programming Language	Sylla	bus	2023-2	4

Course Objectives:

The main objectives of this course are to:

- 1. This course presents an overview of elementary data items, lists, dictionaries, sets and tuples
- 2. To understand and write simple OOP WITH C++ Python programs
- 3. To Understand the OOPS concepts of Python
- 4. To develop web applications using Python

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

1 To write programs in Python using OOPS concepts	K1,K2
2 To understand the concepts of File operations and Modules in Python	K2,K3
3 Implementation of lists, dictionaries, sets and tuples as programs	K3,K4
4 To develop web applications using Python	K5,K6

K1-Remember; K2-Understand; K3-Apply; K4-Analyze; K5 -Evaluate; K6 -Create

LIST OF PROGRAMS 45 hours		
	LIST OF PROGRAMS	45 hours

OBJECT ORIENTED PROGRAMMING WITH C++ (Any 6)

- 1. Create a class to implement the data structure STACK. Write a constructor to initialize the TOP of the stack to 0. Write a member function POP() to delete an element. Check for overflow and underflow conditions.
- 2. Create a class ARITH which consists of a FLOAT and an integer Variable. Write member ADD(), SUB(), MUL(), DIV(), MOD() to perform addition, multiplication, division and modulus respectively. Write member functions to get and display values.
- 3. Create a class MAT has a 2-d matrix and R&C represents the rows and columns of the matrix. Overload the operators +, -, * to add subtract and multiply two matrices. Write member functions to get and display MAT object values.
- 4. Create a class STRING. Write member function to initialize, get and display strings. Overload the operator + to concatenate two strings, = = to compare two strings and a member function to find the length of the string.
- 5. Create a class which consists of EMPLOYEE detail like eno, ename, dept, basic-salary, grade. Write member functions to get and display them. Derive a class PAY from the above class and write a member function to calculate da, hra, pf depending on the grade and Display the Payslip in a neat format using console I/O.

- 6. Create a class SHAPE which consist of two VIRTUAL FUNCTIONS Cal_Area() and Cal_PERI to calculate AREA and PERIMETER of various figures. Derive three classes SQUARE,RECTANGLE and TRIANGLE from the class SHAPE and calculate AREA and PERIMETER of each class separately and Display the result.
- 7. Create two classes which consists of two private variables, one float And one integer variables in each class. Write member functions to get and display them. Write FRIEND function common to arguments. And the integer and float values of both the objects separately and Display the result.
- 8. Write a user defined function USERFUN() which has the formatting commands like setw(), showpoint, showpos precision(). Write a program which prints an multiplication table and uses USERFUN() for formatting.
- 9. Write a program to perform Insertion, Deletion and Updation using files.
- 10. Write a program which takes a file as argument and copies in to another file with line numbers using Command Line Arguments.

PYTHON PROGRAMMING LAB (Any 6)

Implement the fo<mark>llowing in Python:</mark>

- 1. Write a Python program by getting input from the user and displays the following information: Your name, Full address, Mobile number, College name, Course, Subjects.
- 2. Write a Python program to find the largest three integers using if-else and conditional operator.
- 3. Write a Python program that asks the user to enter a series of positive numbers (The user should enter a negative number to signal the end of the series) and the program should display the numbers in order and their sum.
- 4. Write a Python program to find the product of two matrices [A] m x p and [B] p x r.
- 5. Write recursive functions for Fibonacci sequence up to given number n.
- **6.** Write recursive functions to display prime number from 2 to n.
- 7. Write a Python program that writes a series of random numbers to a file from 1 to n and display.
- **8.** Write a Python program to sort a given sequence: String, List and Tuple.
- **9.** Write a Python program to make a simple calculator using functions.
- **10.** Write a Python program for Linear Search using functions.
- 11. Write a Python program for Inheritance
- 12. Write a Python program to slice a given list.
- 13. Write a Python program to count the number of words.
- 14. Write a Python program to copy a file.
- 15. Write a Python program to check the given password is correct or not using regular expressions.

	Total Lecture hours	45 hours
T	ext Books	
1	Bill Lubanovic, "Introducing Python", O'Reilly, First Edition - Second Release	se, 2014.
2	Mark Lutz, "Learning Python", O' Reilly, Fifth Edition, 2013.	
	Course Designed By:	
	Dr.K. Venmathi, Assistant professor, L.R.G. Govt Arts College for Women,	Tirupur &
	Mr S Shankar Assistant Professor Sri Vasavi College SF Wing Frod	ρ





SCAADATED:18.06.2024 ,REVISED JUNE 2024

~ -		8051 MICROCONTROLLER AND	_			~
Course code		ITS APPLICATIONS	L	T	P	C
Core/El		Core paper VI	5			4
Suppo		D. v. 1	G 11		2022	- 24
Pre-rec	quisite:	Digital Electronics8085	Sylla		2023	-24
		Microprocessor	Vers	ion		
Course Obj	ectives:	Meroprocessor			1	
-		this course are to:				
		itecture and addressing modesof8051				
To Im	part knowle	edge about assembly language programsof8051				
		importance of different peripheral devices and				
	nterfacingto					
		edge <mark>of different types of external interfaces incl</mark> udingLC	D,Ke	ypac	l	
Matrix	x, Stepper n	notorandsensors				
Expected C						
		pletion of the course, student will be able to:				
		re and operation of Microcontroller 8051			K1	
		derstand the design concept of interfacing Microcontrolle	er wit	h	K2)
	peripherals	The state of the s			TVC	
		ansfer and interfacing techniques			K3	
	· ·	derstand the role of embedded systems in industry			K4	
		ansfer through serial and parallel ports.			K5	,
K1 - Reme	mber; K2 -	Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 –	Crea	te	
			<u> </u>			
Unit:1	G ₀	MICROCONTROLLER OVERVIEW		/	18 hour	S
		ntroller – Compar <mark>ison of µP & µC</mark> - 8051 Microcontrolle				
		ock – A, B & Register Banks – Stack - Program Counter				.—
- Flag & PS	W – Specia	l Function Registers – Internal Memory – Input / Output	Pins	& P(orts.	
Unit:2	ADI	DRESSING MODES, ARITHMETIC AND LOGICAL	<u> </u>		18	
CIIIC.2	, in the second	INSTRUCTION	_		hour	S
Introduction	– Addressi	ng Modes: Direct – Indirect – Register - Indexed – Arith	metic			
		Multiplication, Division, Increment And Decrement –				
Instructions:	AND, OR,	XOR, CPL & Compare – Rotate & Swap Instruction: R	R, RL	, RR	C, R	LC.
TI		DATE A TED ANGEED AND DD ANGIL OPED ATTO	NIC		10	
Unit:3		DATA TRANSFER AND BRANCH OPERATION	N5		18 hour	·C
Introduction	– Internal I	Data Move - External Data Move – Code Memory Read	Only l		<u> </u>	
		ions – Conditional Jump – Unconditional Jump – Call Ir	•			-
-	-	Push and Pop Instructions – Simple Programs.				
Unit:4		PERIPHERALS			8hou	
		ner Registers – TMOD – TCON - Mode 1 and 2 Program	_			•
	_	MAX232 – Baud Rate – SBUF Register – SCON Register	er – Pi	ogra	m to	
Transfer and	Keceive D	ata Serially.				

Unit:5	INTERFACING	18hours
Interfaci	ing of LCD - LCD Operation - Pin Descriptions - Command and Data to the	LCD –
ADC 0804	 LM34 Temperature Sensor – Stepper Motor – Interfacing Key Board. 	
		T
	Total Lecture hours	90 hours
Text Boo	ok(s)	
	ed Ali Maszidi & Janice Gillispie Maszidi, "The 8051 Microcontroller and	
Embed	ded System", Pearson Publishers	
	h J. Ayala, "The 8051 Microcontroller Architecture, Programming and	
Applica	ation"2 nd Edition, Penram Int ernational Publications	
Reference	ce Books	
1 Myke p	redko, "Program <mark>ming and Customizing the 8051 Microcontroller"</mark> , Tata I	McGraw
Hill, 1 st	Edition	
	K., —The80 <mark>51 Microcontr</mark> oller:Architecture, pro <mark>gramming an</mark> d application	15",
	International (2005) 3rd edition.	
	Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
	vww.youtube.com/watch?v=84YUQu8tE4w	
	vww.youtube.com/watch?v=GPz_mR7Flas	
3 https://v	vww.youtube.com/watch?v=uFhDGagZzjs	
	Course Designed By:	
	Or.K.Ven <mark>mathi ,Ass</mark> is <mark>tant professor,L.R.G.Govt Arts Colleg<mark>e for W</mark>omen, Tiru</mark>	ıpur &
	S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode	

					The state of the s					
Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	S	M	M	M	S	Lie	L	S
CO2	S	S	S	M	M	M	Soll	L	L	L
CO3	S	S	S	M	LILM 60 ATE TO	OU B-	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

Course Code	e 53B	INTERNET OF THINGS	L	T	P	С							
Core/Electiv /Supportive:		CORE PAPER VIII	5			4							
Pre- Requisite:		Basic Electronics	Sylla Vers		2023-24								
Course Obje	ctives:												
To en	able the stud	this course are to: dents to learn about IoT and also to under ices and Interfacing sensors.	stand the	e con	icep	i.							
Expected Co	urse Outco	mes:											
On the Succe	ssful compl	etion of the course, student will be able to):										
		of basic IoT				K1 K2							
	1 1												
		bout embedded devices				K3							
		sensor Interface technology				K4							
	ze the IoT a				K4								
K1:Rememb	er; K2 -U	Understand; K3 -Apply; K 4-Analyze; I	(5 -Eval	uate;		K6-Create							
Unit:1		IOT FUNDAMENTALS				18 hours							
IoT – Techr	ologies for	IoT – Applications of IoT – Industrial Io DESIGN PRINCIPLES FOR CONNI DEVICES				hour s							
		systems - Communication Technologies - ce management - Ease of Designing and A		_		nt, data							
Unit:3		RAMMING FUNDAMENTALS WITH USING ARDUINO IDE				8 hours							
	and Loops	Syntax – Data Types/ Variables/ Constant – Using Arduino C Library Functions	_										
Unit:4		SENSORS AND ACTUATORS			1	8 hours							
-	_	sors – Interfacing temperature sensor, ultra to – Interfacing LED and Buzzer with Ar		senso	or ar	nd infrared							
Unit:5	SEND	ING SENSOR DATA OVER INTERN	ET			18 hours							
		6 NODEMCU WiFi Module – Programn ViFi and NODEMCU to transmit data from											
		Total Le	ecture h	ours		90 Hours							

Text B	ook(s)
1	Arshdeep Bahga, Vijay Madisetti, — Internet of Things:A Hands-On Approach ", 2014. ISBN: 978-0996025515
2	Boris Adryan, Dominik Obermaier, Paul Fremantle, — The Technical Foundations Of Iot ", Artech Houser Publishers, 2017.
Refere	nce Books
1	Michael Margolis,—Arduino Cookbook", O"Reilly, 2011
2	Marco Schwartz, —Internet of ThingswithESP8266", Packt Publishing,2016
Relate	d Online Contents [MOOC, SWAYAM, NPEL, Website etc.]
1	https://nptel.ac.in/courses/106/105/106105166/Introduction to IoT Part I – Lecture 1
2	-https://ocw.cs.pub.ro/courses/iot/courses/02Electronics for Internet of Things –
	Lecture II
3	https://nptel.ac.in/courses/106105166/Introduction to Arduino – I – Lecture 22
	Course Designed By:
R.Aı	rchana, Assistant professor, Nehru Arts and Science College, Coimbatore.&
	I Om Muruga, Assistant Professor, Government Arts College Ooty,
C. N	Omprakash Anand, Assistant Professor, Government Arts College, Ooty.

Mappi	ng with <mark>P</mark> ı	<mark>rogram</mark> n	ne Outo	comes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	S	L	L	L
CO3	Sol	S	S	M	M	L	L	L	M	M
CO4	M	M	M	S	S	S	L	E Lo	M	M
CO5	M	M	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low FOUCATE TO ELEVATE

Course code	5ZC	VISUAL PROGRAMMING	L	,]	Γ	P	C		
Core/Elective/ Supportive:		Skill Based Subject III:	3				3		
Pre-requisite		Computer skills and familiarity with Microsof Windows.	-	abus sion	20	23-2	24		
Course Objecti	ves:	1			1				
♣ To Emphadocument	and develoasis on the ation	s course are to: op Windows-based applications using Visual Basic fundamentals of design, development, implementa about to write visual C++programming	tion and	d					
Expected Cours	se Outcom	nes:							
		tion of the course, student will be able to:							
1 Explore Vis	sual Ba <mark>sic'</mark>	S				K1	ĺ		
2 Implement	syntax rule	es in Visual Basic programs				K	2		
3 Explain var	riables a <mark>nd</mark>	data types used in program development				K3	3		
Write and apply visual C++ principles and programming techniques									
5 Write and a	ppl <mark>y pr</mark> oce	dures, sub-procedures, and functions to data trans	fer			K5	5		
K1 - Remembe	er; <mark>K2 -</mark> Un	<mark>id</mark> erstand; K3 - Ap ply; K4 - Analy ze; K5 - Eval <mark>ua</mark>	te; K6 -	– Crea	ate	4			
Unit:1	1 5	VISUAL BASIC OVERVIEW				9hou	ırs		
Window Messag	ge – Dev	Programming – Event Driven Programming – Da ice Context – Document Interfaces – Dynam pols – Context Help.							
Unit:2	9	VISUAL BASIC OBJECTS	13		7	9hoi	ırs		
MenusandToolba Multiple Docum Applications.	ars–UsingD	- Forum Design - VBX Controls - Properties - E DialogBoxes-WorkingwithControlArrays-ActiveX ace (MDI) - File System Controls - Data Control	Control – Datab	S					
Unit:3		BUILDING THE USER INTERFACE				9hoi			
Handling – Mess	sage Dispa Architectu	 Frame Work Classes – VC++ Components ttch System – Model and Modeless Dialogs – Impore – Serialization – Multiple Document Interfactors 	ortant	VBX	Co	ontro	ols –		
Unit:4		DATABASE AND APPLICATIONS			9) ho	urs		
defined DLL's	Dialog	Min Database Applications – Embedding Control Based Applications – Dynamic Data Transfer - Communicating with other applications – Object	Function	on –					
Unit:5		DESIGN AND DEVELOPMENT			9) ho	urs		
Basics of GUI D Simultaneous M		sual Interface Design – File System – Storage and rm Development	Retriev	al Sys	ster	n			
		Total Lecture	nours	45 h	our	'S			

T	ext Book(s)
1	Petzold, Windows Programming , Microsoft Press, 1995
2	Marion Cottingham, Visual Basic , Peachpitt Press, 1999
3	Kate Gregory, Using Visual C++ , Prentice Hall of India Pvt. Ltd
4	PapparandMurray, VisualC++: The Complete Reference , Tata McGraw Hill, New Delhi, 2000.
R	leference Books
1	John Paul Muller, "Visual C++ from the Ground Up", Tata McGraw Hill, Delhi.
2	Richard C. Leinecker and Tom Archer, "Visual C++ 6 Programming Bible", Wiley India Pvt Ltd.
R	telated Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]
1	https://www.youtube.com/watch?v=5nahqfJTQXs
2	https://www.youtube.com/watch?v=1oGpl6qNKoQ
3	https://www.youtube.com/watch?v=gcFHyVYdeFU

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

						A STATE OF THE PARTY OF THE PAR				
Mappi	ng with P	<mark>rogr</mark> amn	ne Outo	comes	7		- 5 4			
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
			100	1		and the same				
CO1	S	S	S	M	M	M	S	L	L	S
		100			11					
CO2	S	S	S	M	M	M	S	L	L	L
	\ \ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\	A								
CO3	S	S	S	M	M	L	L	S	S	M
		9		MI	AR			6		
CO4	M	M	M	S	S	S	L	$^{\circ}$ L	M	M
		1	2/3				0			
CO5	M	M	S	OS	S	L	M	M	S	S
				ా చిత్ర	LILITE) I 2_11				

^{*}S-Strong; M-Medium; L-Low



Course code	63A	DESIGN WITH PIC MICROCONTROLLER	L	T	P	С				
Core/Electi Supportiv		Core paper VIII	6			4				
Pre-requis		Students having knowledge of electronics fundamentals coupled with some programming experiences are the ideal participants for this course.	Syllal Versi		202	3-24				
Course Objecti										
J		this course are to:								
knowledg	ge abou	t assembly language programs of PIC Microcontroller PICMicrocontroller								
		s peripheral devices and to interface them with PIC Microco	ontrolle	er						
Expected Cour										
		repletion of the course, student will be able to:			K					
	5,									
		l architecture model and programming techniques			K					
		ration and need for interrupts and timers			K					
		operators in modelling the design units			K					
		ot for I/O port expansion			K	5				
K1 - Rememb	er; K2 -	 Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K CPU ARCHITECTURE AND INSTRUCT 			1	ours				
		rchitecture and Pipelining – Program Memory Consideration g Modes – CPU registers – Instruction Set – Simple Oper LOOP TIME SUBROUTINE, TIMER 2 AND		Regist		le nours				
		INTERRUPTS	9							
Routine – Loop				ıpt Se						
Unit:3		EXTERNAL INTERRUPTS AND TIME				nours				
Programmable 1	Period 3	errupt Input — Timer 0 — Compare Mode — Capture M Scalar — Timer1 External Event Counter — Timer1 and uts — Port B Change Interrupts								
Unit:4	1	JO PORT EXPANSION			15h	ours				
Synchronous Se Expansion – LC		t Module – Serial Peripheral Interface – Output Port Expar lay	nsion –	- Inpu	t Po	rt				
Unit:5		I ² C BUS FOR PERIPHERAL CHIP ACC	CESS		15h	ours				
I2C Bus Operati	on - I20	C Bus Subroutines – DAC Output – Temperature Sensor –	Serial	EEPI	ROM	1				
		Total Lecture hours			75 ł	ours				
Text Book(s) 1 John B.Peatm	nan,— D e	esign with PIC Microcontrollers—,Pearson Education Publi	shing							
		, , , , , , , , , , , , , , , , , , ,	<u>U</u>							
Reference Boo	ks									
		zidi, Rolin D. McKinlay, and Danny Causey, "The PIC dEmbeddedsystems—UsingAssemblyandCforPIC18, Prent	ice							

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://www.youtube.com/watch?v=nLlBssKCN2w
- 2 https://www.youtube.com/watch?v=VEAYB1A9SiA
- 3 https://www.youtube.com/watch?v=aSsnLyKtIAU

Course Designed By:

Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes													
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10				
CO1	S	S	S	M	M	M	S	L	L	S				
CO2	S	S	S	M	M	M	S	L	L	L				
CO3	S	S	S	M	M	L	L	S	S	M				
CO4	M	M	M	S	S	S	L	L	M	M				
CO5	M	M	S	S	S	L	M	M	S	S				

^{*}S-Strong; M-Medium; L-Low

Course code 63P	Internet of Things System Design Lab	L	Т	P	C
Core/Elective/ Supportive	Core –Practical- VI			3	4
Pre-requisite	Basic knowledge of Electronic Circuits or permission of instructor	Syllabus 20 Version		202	23-24
Course Objectives:					

The main objectives of this course are to:

- ❖ To make the students to design IoT based circuits
- To understand the characteristics of Arduino & sensors.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

011	the successful completion of the course, student will be use to.	
1	Interfacing the system and IoT	K4
2	Understand the characteristics of Internet of Things.	K6
3	Design the smart works	K5
4	Design Various Applications using IoT	K5
5	Understand the Sensors methods	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

(ANY 8 EXPERIMENTS)

- 1. Installation and Library Management
- 2. LED interfacing.
 - [i] Blinking
 - [ii] Running Light
- 3. Switch Interfacing
 - [i] Without Interrupt
 - [ii] With Interrupt
- 4. Serial Communication & Plotter
- 5. ADC Interfacing
- 6. Buzzer interfacing
- 7. Tone Generation
- 8. LCD interfacing

- 9. Servo Motor interfacing
- 10. Joystick interfacing
- 11. Tilt Sensor interfacing
- 12. Reed Switch interfacing
- 13. Thyristor protection circuit

Course Designed By:

Dr.T.Siva Kumar, Principal, RVS College Of Arts and Science, Coimbatore & Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur.

Mapping	with Pro	<mark>gramme</mark>	Outco	mes		- De	TO THE			
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	9L	L
CO3	S	Sog	M	M	M	L	L	GE	S	M
CO4	S	S	M	95 55	S	S U	Lan	L	S	M
CO5	M	M	S	LSU	ATE TO	ELLAT	L	L	S	M

^{*}S-Strong; M-Medium; L-Low

B.Sc. Electronics 2023-24 onwards-Affiliated Colleges - AnnexureNo.30(a)(1)

SCAADATED:18.06.2024, **REVISED JUNE 2024**

Course code	63Q	INDUSTRIAL AND POWER ELECTRONICS	L	1	P	C
Core/Ele Suppor		Core –Practical- VII			3	4
Pre-rec	juisite	Basic knowledge of Electronic Circuits or permission of instructor	Syllah Versi		202	23-24

Course Objectives:

The main objectives of this course are to:

- ❖ To make the students to design triggering circuitsofSCR.
- ❖ To understand the characteristics of powerelectronic devices.

Expected Course Outcomes:

On the successful completion of the course, student will be able to:

	r	
1	Design triggering circuits of SCR	K4
2	Understand the characteristics of power electronic devices.	K6
3	Design power Inverter Circuits	K5
4	Design Various Applications of SCR	K5
5	Design Cyclo Converter Circuits	K5

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

Unit I

INDUSTRIAL AND POWER ELECTRONICS

90 Hours

(ANY 16 EXPERIMENTS)

- 1. Triggering of SCR by R, C and Diac.
- 2. Design of snubbercircuit.
- 3. Fan regulator using Triac.
- 4. Thyristorchopper.
- 5. TRIACFlasher.
- 6. CommutationTechniques.
- 7. Speed control of DC motor using SCR.
- 8. Automatic street light controller
- 9. Burglar Alarm
- 10. Sequencer Circuit.
- 11. Power Inverter
- 12. Switching Regulators
- 13. Automatic Battery Charger
- 14. Firealarm
- 15. ON / OFF relay control using opto coupler
- 16. Servo stabilizer
- 17. Layout and Art Work preparation for PCB
- 18. Etching Drilling and Component mounting of PCB
- 19. Temperature controller using AD 590/LM 35.
- 20. Construction of EmergencyLamp.
- 21. Phase Control Circuit
- 22. Cycloconverter
- 23. Thyristor protection circuit

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr.S. Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mapping	g with Pro	gramme	Outco	mes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	L	L	L	S	M



Course code 63R	8051 MICROCONTROLLER	L	Т	P	С		
Course code osk	LAB		1	1	C		
Core/Elective/	Core –Practical – VIII			3	4		
Supportive:							
Pre-requisite	DigitalElectro	Sylla	bus	2023-24			
_	nics8085	Vers					
	Microprocessor						
Course Objectives:							
The main objectives of th	is course are to:						
	ssembly language programming of Microcontroller						
_	lent's Assembly language programming skills and give	s prac	tical t	raini	ing of		
interfacing the peri	pheral devices with the Microcontroller						
Expected Course Outcon							
On the successful compl	etion of the course, student will be able to:						
1 Apply the fundament	als of assembly level programming of microcontroller			K	.3		
2 Design and Develop	program for real time interface			K	.6		
K1 - Remember; K2 - U	nderstand; K3 - Apply; K4 - Analy <mark>ze; K5 - Evalu</mark> ate;	K6 – (Create	2			
2. Multiplica 3. BlockData 4. Smallest / 5. To Arrang 6. Sum of N 7. 1's and 2's 8. UP/DOWN 9. Traffic Lig 10. WaveFor 11. ADCInter 12. DACInter 13. StepperM 14. Solid Stat 15. DCMotor 16. Temperat 17. Rolling a 18. LCDInter	Largest of NNumbers e in Ascending / Descending Order 8 bit Numbers c Compliment of an Array (8 / 16bit) N Counter using 7 Segment Display tht Control Interface m Generation rface rface totor Interface te Relay Interface ure Controller and Blinking of a Message fface	97/0					
19. Frequenc	•						
20. WaterLev	elIndicator						

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode..

Mappi	ng with P	rogramn	ne Outo	comes						
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	Lo L	L	L	S	M



Course code	6ZP	VISUAL PROGRAMMING LAB	L	T	P	C
Core/Ele Suppor		Skill Based Subject Practical-IV			3	2
Pre-requisite		Basic computer skills and familiarity with Microsoft Windows.	Sylla Vers		20	23-24

Course Objectives:

The main objectives of this course are to:

Learn to design and develop Windows-based business applications using Visual Basic Emphasis on the fundamentals of structured design, development, implementation, and documentation

Expected Course Outcomes:				
On the successful completion of the course, student will be able to:				
1 Implement syntax rules in Visual Basic programs	K2			
2 Explain variables and data types used in program development	K3			
3 Write visual C++ principles and programming techniques	K6			
4 Design Application Editor and Control	K5			
5 Design VB application Link	K5			

K1 - Remember; K2 - Understand; K3 - Apply; K4 - Analyze; K5 - Evaluate; K6 - Create

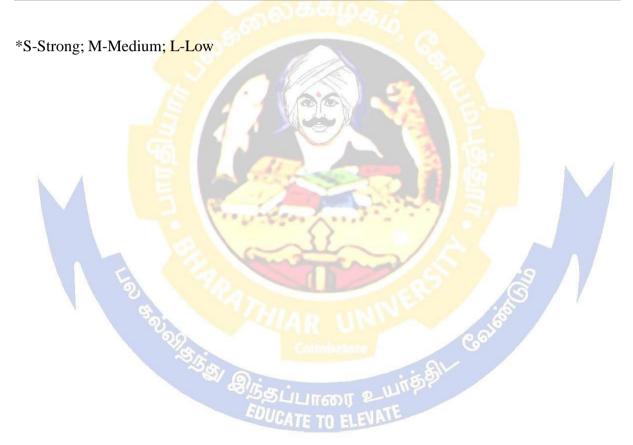
VISUAL PROGRAMMING (ANY 8 EXPERIMENTS)

- 1. Building Simple Applications using Basic Tools
- 2. Working with Intrinsic Control and ActiveXcontrols
- 3. Create an Application with Multiple Forms and Dialogs
- 4. Write a VB Program to Design an e-mailRegistrationForm
- 5. Create an Application with Menu Editor
- 6. Create an Application with DAO Controls
- 7. Create an Application using Common Dialogs
- 8. Write a program for Drag andDropEvents
- 9. Create a Database for library management using ADDC ontrols
- 10. Creating an Application using ActiveXcontrol
- 11. Create a Scientific CalculatorinVB
- 12. Develop a VB application to either link or Embed MS Word Document to an OLEControl
- 13. Display Student Information using Grid Control
- 14. Create an Application using RDOControls
- 15. Develop an Application to perform the following operation in the Employee Tableusing DAO
 - i) Add anewRecord.
 - ii) DeleteaRecord.
 - iii) ModifyaRecord.

Course Designed By:

Dr.K. Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	M	M	S	S	S	M	S	M	M	S
CO2	S	S	S	M	M	M	S	M	L	L
CO3	S	S	M	M	M	L	L	L	S	M
CO4	S	S	M	S	S	S	L	L	S	M
CO5	M	M	S	S	S	L	L	L	S	M





B.Sc. Electronics 2023-24 onwards-Affiliated Colleges - AnnexureNo.30(a)(1)

SCAADATED:18.06.2024, **REVISED JUNE 2024**

Course Code	5EA	PCB DESIGN AND FABRICATION	L	T	P	С	
Core/Elective/S	up	Elective-I-A	4			3	
portive							
Pre-requisit	e function	knowledge of circuits familiar with the s and performance of various components have a good logical thinking ability.	Sylla Vers		2023-24		
Course Objectives:							
The main objec	tives of this co	urse are to					
		dge ofPCBdesign					
		n various methods of laying outaPCB					
	now to etchand	• •					
		niques used for PCB designandfabrication					
Expected Cours		- Was Colors					
•		f the course, student will be able to:					
	y the boa <mark>rds ar</mark>				K1		
2 Design	n layout <mark>and m</mark> a	ake use of the photo printing and etching tech	nique	S	K2	,	
l l		n rules and automation techniques			K3		
4 Under	stand ba <mark>sic cor</mark>	ncepts of transmission line, crosstalk and ther	<mark>mal i</mark> s	sues	K4	•	
5 sign	,	d layout) PCB for analog circuits, digital circuits	uits ai	nd miz	xed K5		
K1:Remember;	K2-Underst	tand; K3- Apply; K 4-Analyze; K5- Evaluat	te;	K6- C	Create		
Unit:1	E	TYPES OFPCB			18 l	nours	
Single sided boa	ard <mark>– double si</mark>	de <mark>d – Multilayer b</mark> oards <mark>– Plated thro</mark> ug <mark>h hol</mark> e	es tec	hnolo	gy –		
		chnology (SMT) – Limitation of SMT – Surfa Diode and IC's	ice mo	ount c	omponents:		
Unit:2		LAYOUT AND ARTWORK	1	9	181	nours	
Spacing – Supp and Package De	ly and Groun <mark>d</mark> nsity – Layout	yout of Layout – Resistance, Capacitance a Conductors – Component Placing and Mount Check Artwork Taping Guidelines – General Artw	nting -	- Coo	ling Require	ement	
Unit:3	I	AMINATES AND PHOTO PRINTING			18 l	nours	
Cleaning Proces	Manufacture of Copper Clad Laminates – Properties of Laminates – Types of Laminates – Manual Cleaning Process – Basic Printing Process for Double Sided PCB's – Photo Resists – Wet Film Resists – Coating Process for Wet Film Resists – Exposure and Further Process for Wet Film						
Unit:4		ETCHING AND SOLDERING			18 l	nours	
Introduction – Etching Machine – Etchant System - Soldering: Principles of Solder Connection – Solder Joints – Solder Alloys – Soldering Fluxes - Soldering Tools: Soldering – De-soldering Tools and Techniques – Man Soldering – Solder Mask – Safety, Health and Medical Aspects in Soldering Practice						ion – Tools	
ļ	Unit:5 DESIGN RULES AND AUTOMATION 18 hours						
Unit:5	DES	IGN RULES AND AUTOMATION			18 h	ours	
Reflection – C	Crosstalk – Gro	ound and Supply Line Noise – Electromagneti	ic Inte	rferer			
Reflection – C	Crosstalk – Gro		ic Inte	rferer	nce from Pul		

Text Boo	$\mathbf{k}(\mathbf{s})$
1	Walter C. Bosshart, —PCB Design and Technology, Tata McGraw Hill Publications,
	Delhi 1983
2	RS Khandpur, —Printed Circuit Board by Tata McGraw Hill Education Pvt Ltd., New Delhi
Referenc	e Books
1	S D Mehta, —ElectronicProduct Design Volume-I, S Chand Publications
Related (Online Contents [MOOC, SWAYAM, NPEL, Website etc.]
1	https://www.wikihow.com/Create-Printed-Circuit-Boards
2	http://www.siongboon.com/projects/2005-09-07_home_pcb_fabrication/
3	https://reprap.org/wiki/MakePCBInstructions#Making_PCBs_yourself
4	https://www.youtube.com/watch?v=mv7Y0A9YeUc
4	https://www.youtube.com/watch?v=imQTCW1yWkg
	Course Designed By:
	Dr.K. Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur &
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
	4	111			The same of			9		
CO1	S	S	S	M	M	M	S	L	L	S
			100	with the	2000	1.7 -				
CO2	S	S	S	M	M	M	S	L	L	L
		2		5	3	(Á		
CO3	S	S	S	M	M	L	L	S	S	M
					7					
CO4	M	M	M	S	S	S	L	L	M	M
		2006			AIN			060		
CO5	M	M	S	S	S	rior L	M	M	S	S
			50				112			

^{*}S-Strong; M-Medium; L-Low

Course code 5EB	ADVANCED COMMUNICATION SYSTEMS	L	T	P	C		
Core/ Elective/	Elective I B	4			3		
Supportive:							
Pre-requisite	Principles of Communication Systems	Syllabus 2023- Version					
Course Objectives:							
The main objectives of							
•	ous data communicationsystems						
	dents understand the basic concept in the field of pulse co	mmuni	cation	ns an	d		
cellular commu							
	of satellite communication and expose the learners to the b	asics of	tsigna	al			
propagation thro	ough opticalfibers						
F 4 10 0 4	600						
Expected Course Out							
	npletion of the course, student will be able to:						
=	rent types of Pulse communication systems			K			
, ,	or <mark>mance</mark> of modulation techniques			K			
	Satellite and cellular communication system			K			
	rmance of cellular communication systems			K			
5 Study the principl	e of optical fibre structure and its various applications			K	4		
K1 - Remember; K2	<mark>- Under</mark> stand; K3 - A <mark>pply; K4</mark> - An <mark>4alyze; K5</mark> - <mark>Evaluate</mark>	; K6 - (Create				
Unit:1	DATA COMMUNICATION		18	hour	'S		
Introduction – Data Fo	orm <mark>s – Transmission Modes – Simplex, Half Duplex</mark> – I	Full Du	plex	– Po	oint		
	ır N <mark>etwork – Ring Netw</mark> ork – Bus Network <mark>– Telepho</mark> ne I						
	ata Communication System – Asynchronous and Synchro	onous T	ransn	nissi	on –		
USART's and ACIA's	- RS 232 Inter Connect Cable - Modems - Protocols	9/					
Unit:2	PULSE COMMUNICATION		18	hou	rs		
Basic Concepts of Puls	se Modulation – Sam <mark>pling Theorem –</mark> PAM – PTM – PFN	M - PPI	M - P	P CM			
 Differential PCM- D 	elta Modulation–Ada <mark>ptive Delta M</mark> odulation–TDM– FD	M-AS	K–FS	K– l	PSK		
Unit:3	SATELLITE CONMMUNICATION		18	hou	rs		
Introduction – Satellite	e Orbit – Satellite Position – Up link – Down Link – Cross	s Link –	Assi	gnał	ole		
Satellite Frequencies	EDUCATE TO ELEVALE						
	onder – Antenna System – Power Package and Station K		– For	ms c	of		
Modulation–Free Path	Space Losses–Ground Station– Aligning the Satellite Dis	sh					
Unit:4 CELL	ULAR COMMUNICATION SYSTEM		18l	our	S		
Introduction Cellular	Mobile System - Basic Cellular System - Operation	nal Cel	lular	Sys	tem		
Maximum number of	f Calls per Cell - Maximum Number of Frequency	Channe	1 - 0	Conc	ept (
	ell Splitting – Permanent Splitting – Real Time Splitting -	- Frequ	ency				
Management – Chann	el Assignment						
Unit:5	OPTICAL COMMUNICATION		1 Q L	our			
	Fibers – Optical Fiber Structure – Numerical aperture –	Dropos			•		
	pplicationsofOpticalFiber(Videolink,Satellitelink,Compute		auon	OI			
I TOUTH SWEETITCH TOUT A							
Communicating Anter		ziiiik,					

Total Lecture hours

90hours

Text Book(s	
1 Robe	rt J. Shoernbeck,-ElectronicCommunicationsModulationandTransmission,
PHI	,1999
	okSingh, -PrinciplesofCommunicationEngineering , S.Chandand Company, 2 nd
Editio	
3 Sanj	eev Gupta,-ElectronicCommunicationSystem,KannaPublishingCompany
Reference I	Books
1 Data	communications and networking (sie) By behrouz a. Forouzan (author)
2 Elect	ronic Communications 4th Edition by Dennis Roddy and J Coolen, Pearson Education
Related On	line Contents [MOOC, SWAYAM, NPTEL, Websites etc.]
1 https:	//nptel.ac.in/courses/108/101/108101113/
2 https:	//nptel.ac.in/courses/117/105/117105143/
3 https:	//nptel.ac.in/courses/106/106/106106167/
	Course Designed By:
Dr.K	X. Venmathi , Assistant professor, L.R.G. Govt Arts College for Women, Tirupur &
N	Ir. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.
ı	

										4
Mappi	ng with P	<mark>rogr</mark> amn	ie Outo	omes	1	7. 7				
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
			100	P. C	1000	1.7 " -	/			
CO1	S	S	S	M	M	M	S	L	L	S
	7			Y	1					
CO2	S	S	S	M	M	M	S	L	L	L
			196						S	
CO3	S	S	S	M	M	L	L	S	S	M
		000			all			0.6		
CO4	M	M	M	S	S	S	L	L	M	M
			्डा							
CO5	M	M	S	S	S	الرط س	M	M	S	S
				EDIT	ATE TO	EI EILAT	3			
1					ALL IU	1177			•	

^{*}S-Strong; M-Medium; L-Low

Course Code	5EC	Introduction of Artificial Intelligence	L	T	C
Core/Elective /Supportive:		Elective I -C	4		3
Pre- Requisite:		New Technology Introduction	Sylla Vers		2023-24
Course Object	tives:				
The Main Obj	ectives	of this course are to:			
		ious bio-potentials and working principles of mudents to learn about bio-potentials and medica			nts
Expected Cou	rse Out	tcomes:			
On the successf	ul comp	eletion of the course, student will be able to:	ું હ		
1 U	Jnderst	and the evo <mark>lution in microcontroller technolo</mark>	gy		K2
2 I	Describe	e Harvard arc <mark>hitectu</mark> re model and programmin	ng techn	iques	K2
		the operation and need for interrupts and tim		1 F	K3
4 I	List out	various <mark>operator</mark> s in modelling the design uni	ts	10	K2
5 A	Apply th	ne concept for I/O port expansion	70 3	10	K4
K1:Remembe	r; K	2-Under <mark>stand; K3-Apply; K4-Analyze; K3</mark>	5-Evalu	at <mark>e;</mark>	K6-Create
		La Programme or service	- /	107	
Unit:1		Introduction			18 hours
		Definition – Future of Artificial Intelligence – blem Solving Approach to Typical AI Problems.	Characte	eristics of	of Intelligent Agents – Typical
Unit:2		Problem Solving Methods	: : 8		18 hours
Problem	Solving	g Methods – Search Strategies – Uninformed – In	nformed	– Heuri	stics – Local Search Algorithms
	`	lems – Searching with Partial Observations –			
_		cing Search – Game Playing – Optimal Decision			
Games.		ang search Came Paying Spinna Secision	o III Gui		pina Bena Franking Scoomastic
Unit:3	Knowl	edge Representation			18 hours
Knowle	dge Rer	oresentation – First Order Predicate Logic – I	Prolog 1	Program	 ming – Unification – Forward
		Chaining – Resolution – Knowledge Representa		•	
•		tal Events and Mental Objects – Reasoning System			
Unit:4		Software Agents			18 hours

	AI Applications	18 hours
	AI Applications – Language Models – Information Retrieval – Inform	ation Extraction – Natural
	Processing – Machine Translation – Speech Recognition – Robot – I	Hardware – Perception –
	Moving.	
	Total Lecture	hours 90 Hours
Text Bool	K(S)	
1	S. Russell and P. Norvig, —Artificial Intelligence: A Modern Approa	ch , Prentice Hall,
2	I. Bratko, - Prolog: Programming for Artificial Intelligence, Fourth Edward Wesley Educational Publishers Inc., 2011.	dition, Addison-
Reference	e Books	
1	M. Tim Jones, - Artificial Intelligence: A Systems Approach (Computer Bartlett Publishers Inc.; First Edition, 2008.	alati
2	Nils J. Nilsson, - The Quest for Artificial Intelligence, Cambridge Un	niversity Press, 2009.
	BESTULLIANT STATES	
Related O	https://www.youtube.com/watch?v=i2mZylgP1Fk	
2	https://www.youtube.com/watch?v=4ldv98F7Zng	
	httms://mmtsl.oo.im/ooxyggg/100/105/100105101/	
3	https://nptel.ac.in/courses/108/105/108105101/	

Mappi	Mapping with Programme Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10			
				165	2000	9	Á						
				(P)			- C						
			- 40.				1 0	3. 7					
CO1	S	S	M	M	M	M	S	M	M	S			
							34						
CO2	S	M	M	M	M	M	S	L	L	L			
		G G						15					
CO3	S	S	S	M	M	L	L	L	M	M			
					1								
CO4	M	M	M	S	S	S	L	L	M	M			
				4	5	>							
CO5	M	M	S	S	M	L	M	M	S	M			
		4	160			1		30 /	19	111			

^{*}S-Strong; M-Medium; L-Low

Course Code	5ED	ADVANCED COMPUTER ARCHITECTURE	L	С					
Core/Elective/Supp	portive:	ELECTIVE I-D	4						
Pre-requisit	e:	Basic Computer Architecture	Syllabus 200						
Course Objectives:			l .		ı				
The objectives of this co	ourse are:								
-	-	lamentals of Advanced Computerdesi	_						
	-	nstruction level parallelism, pipelinin	g and m	emory	hierar	chy			
associated withit									
To enhance the k	nowledge on	advancedprocessors.							
F () () ()									
Expected Course Outco		a student will be able to							
		e, student will be able to: computer design principles.				17.1			
		1 0 1	allaliam			K1			
		outer model with instruction level par	anensin	•		K4			
3 Gain the knowledg	,					K2			
		y in developing an advanced computer	er.			K2			
11 7		ts in advanced processors.			~	K3			
		d; K3 – Apply; K4 – Analyze; K5 –		te; K6					
Unit: 1		NCIPLES OF COMPUTER DESIG				Hours			
_		f computer design - Control Units-Ha							
	ncept – Micro	programming - Bus architectures: Un	ii-bus an	d multi	i-bus				
architectures.									
Unit: 2		TRUCTION LEVEL PARALLELIS				Hours			
		fultiprocessors and multi-computers -				ЛD			
		m - Overcoming DataHazards with D	•		_				
	ies with Dyna	mic - HardwareSupport for Extractin	g More	Paralle					
Unit: 3		PIPELINING				Hours			
Basic concepts – Data ha	azards – Instru	action hazards – Influence on instruct	ion sets	– Data	path a	ınd			
	Performance	considerations – Exception handling	•		40.7				
Unit: 4	1 66	MEMORY HIERARCHY				Iours			
		ches-Reducing Cache Misses and Mis		ty - Rec	lucing	Hıt			
		-Issues in Memory Hierarchy design.			40 T				
Unit: 5		CIPLES OF ADVANCED PROCES				Iours			
1 -	U , ,	C Scalar Processors, RISC Scalar Proc	cessors,	Superso	alar				
Processors, VLIW Arch	itectures, veci	tor and Symbolic processors							
		Total Lectu	ure Hou	rs	90 F	Hours			
Text Books									
Kai Hwang,—Adva 1 st Edition, 1992.	ancedcomputer	r architecture ,Tata Mc.Graw Hill Scie	ence/Eng	gineerin	g/Mat	h			
2 D.A.PattersonandJ.J ARM Edition, 2010		-Computerorganizationanddesign, Mo	organKa	ufmann	,				
Reference Books									
1 Hayes,J.P., —Com	puter Architect	ture and Organization , 3rdEdition, Ta	taMc-G	rawHill	, 1998				
·	—Computer O	rganization and Architecture – Design							

Related Online Contents [MOOC,	SWAYAM, NPTEL, Websites etc.]
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- 1 https://nptel.ac.in/courses/106/103/106103206/
- 2 https://www.youtube.com/watch?v=v7iefsovo9M
- 3 https://www.youtube.com/watch?v=L9X7XXfHYdU&list=PLxCzCOWd7aiHMonh3G6QNKq53C6oNXGrX

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

	Ma <mark>pping with Pr</mark> ogram Outcomes											
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	S	L	L	L	L	L	L	L	L	L		
CO2	S	M	L	L	M	L	M	L	L	M		
CO3	S	M	S	L	L	L	L	L	L	M		
CO4	S	S	L	L		L	L	L	L	L		
CO5	S	S	M	M	M	L	M	L	L	M		

*S-Strong; M-Medium; L-Low

SCAADATED:18.06.2024, **REVISED JUNE 2024**

(Course Code	5EE	Arduino and Sensors	L	T	P	C
(Core/Elective/Sup	pportive:	ELECTIVE II-E	4	0	0	3
	Pre-requisi	_	Basic Computer Architecture	•	labus rsion	20	023- 24
Cour	se Objectives:			•		•	
*	To understand to associated with	wledge on fun he concept of it.	damentals of Advanced Computerdesi instruction level parallelism, pipelining advanced processors.	_	nemory	hierar	chy
Expe	cted Course Out	comes:					
			rse, student will be able to:				
	<u> </u>		d computer design principles.				K1
		•	puter model with instruction level par	allelism			K4
	Gain the knowl <mark>ed</mark>						K2
			hy in developing a <mark>n adv</mark> an <mark>ced co</mark> mpute	er.			K2
			pts in advanced processors.		1 T7.6		K3
K1	– Remember; K. Unit: 1	2 – Understan	nd; K3 – Apply; K4 – <mark>Analyze</mark> ; K5 – Arduino and Sensors	Evalua	ite; K6		ate Iours
,	Unit: 2	Embedded	"C":			18 H	lours
Combi	nations - FOR sta		LSE Statements- WHILE statements New Circuit - Introducing Arrays	- Wha	t is tru		
	Unit: 3	1 Practice	Input and Output:		1		lours
	Input - Pushbut	tons- Potentio	ometers - RGB LEDs- Sound Circui	t - Sim	ple not	e - M	usic ·
Music	with functions –	Making a dig	<mark>ital thermome</mark> ter - Seria <mark>l Monit</mark> or - M	1e asurin	g the te	empera	ature -
Hookii		Talking to the	LCD - Bringing it all together	97			
	Unit: 4	Sensors-1:		9 /		18 H	lours
	Introduction - Ph	not <mark>o Cell (Lig</mark> l	n <mark>t Sensor) - Tilt Sensor Ree</mark> d Switch (Magneti	c Field	Detec	tor) -
Piezo I	Element (Vibratio	on sensor)	Colmbatore (%6)				
	Unit: 5	Sensors-2:	1881			18 H	lours
	One Servo - Joys	stick Pan/Tilt l	bracket - Adding a firing mechanism				

		Total Lecture Hours	90 Hours
Tex	at Books		
1	"Introduction	to Arduino", Alan G. Smith, 2011, ISBN: 1463698348 and	ISBN-13: 978-
•	1463698348.		
Ref	erence Books		
1	"Exploring Arduino:	Tools and Techniques for Engineering Wizardry" by Jeremy	Blum, ISBN-
	10 1118549368, Pub	olisher Wiley 2013.	
	,	•	

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://nptel.ac.in/courses/106/103/106103206/
- 2 https://www.youtube.com/watch?v=v7iefsovo9M
- 3 https://www.youtube.com/watch?v=L9X7XXfHYdU&list=PLxCzCOWd7aiHMonh3G6QNKq53C6oNXGrX

Course Designed By:

Dr.T.Siva Kumar, Principal, RVS College Of Arts and Science, Coimbatore & Dr.K.Venmathi, Assistant professor, L.R.G.Govt Arts College for Women, Tirupur.

	Mapping with Program Outcomes											
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10		
CO1	S	L	L	L	L	L	L	L	L	L		
CO2	S	M	L	L	M	L	M	L	L	M		
CO3	S	M	S	L	L	L	L	L	L	M		
CO4	S	S	L	L	L	L	L	L	L	L		
CO5	S	S	M	M	M	L	M	L	L	M		

^{*}S-Strong; M-Medium; L-Low

Course Code	5EF	MICROWAVE AND FIBER OPTIC	L	T	P	C
		COMMUNICATION				
Core/Elective/		Elective II-F	4			3
Supportive:						
Pre-Requisite:		asic Electronics and Principles of		abus	2023	-24
_		Communication systems	Ver	sion		
Course Objectives	:			· ·		

The Main Objectives of this course are to:

- * To inculcate the principle of microwave theory and working of waveguides
- To know the operation and applications of fibreoptic communication
- * To impart knowledge on the working principle of microwave amplifiers and oscillators

Expected Course Outcomes:

On the Succe	aceful complet	ion of the	course stu	idant will b	a oblato
On the Succe	essiui combiei	Ton or the	course, stu	ident will t	de able to.

1	Understand various parameters of waveguide and use of component	K1
_	as per applic <mark>ations.</mark>	
2	Analyze and find applications and limitations of microwave	K4
	Semiconductor devices.	
3	Discriminate different Radars, find applications and use of its	K5
	supporting systems	
4	Understand The structures of Fiber Optics and types	K2
5	Apply Fiber optics in Sensors Application and in Network Design	K3
N 1		

K1 :Remember;	K2-Understand; K3-Apply; K4-Analyze; F	K <mark>5-Evaluate; K6-Create</mark>
Unit:1	INTRODUCTION TO MICROWAVE	S 18 Hours

Introduction – Maxwell's Equation – Amperes Law – Faradays Law – Gauss Law – Wave Equation – Types of Wave Guides – TE and TM Modes – Propagation of TM Waves in Rectangular Wave Guide – TM Modes in Rectangular Wave Guide.

Unit:2	30	MICROWAVE AMPLIFIERS AND	6	18 Hours
7	G.	OSCILLATORS	15	72

Microwave Tubes: - Two Cavity Klystron - Multi cavity Klystron - Reflex Klystron - Traveling Wave Tube (TWT) - Backward Wave Oscillator (BWO) - Magnetron - Applications.

Unit:3 MICROWAVE DEVICES & RADAR 18 Hours

Microwave Transistors – Gallium Arsenide (GaAs) Metal Semi-conductor FET – Varactor Diode – PIN Diode – Schottky Diode – MASER Principle – Applications – RADAR Block Diagram – Classification – Radar Range Equation – Factors Affecting the Range of a Radar Receivers – Line Pulse Modulator – PPI (Plane Position Indicator) – Moving Target Indicator (MTI) – FM CW Radar- Applications.

Unit:4	OPTICAL FIBER	18 Hours
	COMMINICATION	

Basic Fiber Optic System – Frequencies – Fiber Optic Cables – Refraction – Numerical Aperture – Graded Index Cables – Single Mode – Multi Mode – Cable Constructions – Cable Losses – Connectors – Light Sources – Light Detector – Systems Components – Advantages and Disadvantages.

Unit:5	FIBER OPTICS SENSORS AND APPLICATIONS	18 Hours
Intensity	Modulated Sensors - Hybrid sensors- phase sensors - Diffraction Grating Sensors	
•	ometric sensor - Fiber Fabry Perot Fiber Optic sensor - Chemical Senor Comm	
	Television – Networking - Digital VideoTransmission.	J
	Total Lecture hours	90
		Hours
Text Boo	ok(s)	
1	Kennedy; Davis, Electronic Communication Systems", Tata McGraw Hill	
	Publishing Company Limited, III edition	
2	Gerd Keiser, —OpticalFiber Communication", McGrawHillPublication, IV th	
	Edition, 2011.	
Referen	ce Books	
1	Samuel Y. Liao , "Microwave Devices and Circuits", PHI, III Edition.	
	Govind P. Agrawal, Fiber Optic Communication Systems", Wiley Publicati	ons,
2	IV th Edition, 2012.	
Related	Online Contents [MOOC, SWAYAM, NPEL, Website etc.]	
1	https://nptel.ac.in/courses/108/103/108103141/Introduction to microwave	
	Engineering	
2	https://www.classcentral.com/course/swayam-microwave-engineering-	
	14199 Microwave engineering swayam course	
	Course Designed By:	
I	Dr.K.Ven <mark>mathi ,As</mark> sistant professor,L.R.G.Govt Arts College for Women, Tiru	ipur &
	Mr.S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode	

					_ =				//	
Mappi	ng with P	rogra <mark>mn</mark>	ne Outc	omes					10	
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	M	M	M	M	S	M	M	S
CO2	S	M	M	M	M	M	IT SELL	L	L	L
CO3	S	M	S	M	ATE TO	ELEVAT	L	L	M	M
CO4	S	S	M	S	S	S	L	L	M	M
CO5	S	S	S	S	M	L	M	M	S	M

^{*}S-Strong; M-Medium; L-Low

Course code	5EG	AUTOMOTIVE ELECTRONICS	L	T	P	C
Core/Ele		Elective-II –G	4			3
Suppor Pre-req		Basic Electronics	Syllal Versi		2023	3-24
Course Objec	tives:		1	ı		
 To un autom To un System actuat To un 	derstand the notive system derstand semms, different or derstand, de	s course are to: e concepts of Automotive Electronics and its evolution ms &subsystems overview. msors and sensor monitoring mechanisms aligned to aut t signal conditioning techniques, interfacing technique esign and model various automotive control systems us ment technique.	tomotivesand			
Expected Cou	rea Outcon	noc:				
		etion of the course, student will be able to:				
	an over <mark>vie</mark>				K	2
2 Interface a	automotive	sensors and actuators with microcontrollers			K	4
3 Understar	nd th <mark>e desig</mark>	n cycles, communication protocols and safety systems	employ	ed	K	
	aut <mark>omotive</mark>				K	2
		e management systems			Y	
	_	agement System	V. C		K	4
	ber; K2 - U	nderstand; K3 - Apply; K4 - Analyze; K5 - Evaluate;	K6 – C			
Unit:1		INTRODUCTION			hour	
Wiring System	ms, Circuit	Operation, Electrical Wiring Terminals and Swite Diagrams and Symbols. Charging Systems and les, Alternations and Charging Circuits, Basic Starting	Starti	ng S		
Unit:2	श्	IGNITION SYSTEMS		18	Bhou	rs
Distribution L	Less Ignitio Engine Fue	ctronic Ignition Systems. on, Direct Ignition, Spark Plugs. Electronic Fuel of Illing and Exhaust Emissions, Electronic Control of Injection				
Unit:3]	INSTRUMENTATION SYSTEMS		18	hour	S
	nentation Sy	tation Systems, Various Sensors Used for Different Festems, Vehicle Condition Monitoring Trip Computer,				_
Unit:4	ELECT	TRONIC CONTROL OF BRAKING AND TRACTION		18	houi	rs
of Automatic	Transmissi	ption Control Elements and Control Methodology, I on: Introduction and Description Control Of Gear c Power Steering, Electronic Clutch				

Unit:5	ENGINE MANAGEMENT SYSTEMS	18hours						
Combined Ign	nition And Fuel Management Systems, Exhaust Emission Cont	trol, Digital Control						
Techniques, Complete Vehicle Control Systems, Artificial Intelligence and Engine								
Management,	Automotive Microprocessor Uses. Lighting	and Security						
Systems:Signa	allingCircuit,CentralLockingandElectricWindowsSecuritySystems	5 ,						
Airbags and S	eat Belt Tensioners.							
	Total Lecture hours	90hours						
Text Book(s								
1 TOM DE	NTON, Automobile Electrical and Electronic Systems, Edwar	d Arnold pb., 1995						
Reference B	ooks							
1 1.DON	KNOWLES, Automotive Electronic and Computer controlled	Ignition						
Systems	-	8						
2 WILLIA	M, T.M., Automotive Mechanics, McGraw Hill Book Co.,							
3 WILLIA	M, T.M., Automotive Electronic Systems, Heiemann Ltd., Lond	lon, 1978.						
4 Ronald K	Jurgen <mark>, <mark>Automotive Electronics Handbook</mark>, <mark>McGraw H</mark>ill, Inc</mark>	, 1999.						
Related Onl	ine Contents [MOOC, SWAYAM, NPTEL, Websites etc.]							
1 https://np	tel.ac.in/courses/107/103/107103084/							
	tel.ac.in/courses/107/106/107106088/							
3 https://ww	vw.youtube.com/watch?v=vJ4EfyGXehg							
4 https://ww	vw.youtube.com/watch?v=BG4N2dBgJrQ							
	Course Designed By:							

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar, Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	Mapping with Programme Outcomes									
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	S	M	M	M	S	H	L	S
CO2	S	S	S	M)	M ULIII60	M	MES!	L	L	L
CO3	S	S	S	M	MI	317/10	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

^{*}S-Strong; M-Medium; L-Low

C	ourse Code	5EH	SATELLITE COMMUNICATIONS	L	T	P	C
Co	re/Elective/S	upportive:	ELECTIVE-II-H	4	0	0	3
	Pre-requ	isite:	PRINCIPLES OF COMMUNICATION	•	abus sion	202	23-24
	rse Objective					l e	
	U	this course are	e: n fundamentals of Advanced Computerdes	ion			
	-	_	pt of instruction level parallelism, pipelining	_	nemor	v hierar	chv
	associated		pr or, p.p	-6		, 11101011	
•	To enhance	e the knowled	ge on advancedprocessors.				
		_					
	ected Course						
On s			course, student will be able to:				TZ 1
2			vanced computer design principles. I computer model with instruction level pa	mallalian	<u> </u>		K1 K4
3		yze tile parane owled <mark>ge on pi</mark> j	- 1/4 //-	ranensi	11.		K2
4			erarchy in developing an advanced compu	ter			K2
5			concepts in advanced processors.				K3
			rstand; K3 – Apply; K4 – Analyze; K5 -	- Evalu	ate: K	6 – Cre	
	Unit: 1		ATELLITE SYSTEMS – OVERVIEW				Iours
Intro	oduction- Basi	ic concepts of	Satellite communications- Frequency allocations	cations	for sate	ellite	
syste			ations of sat <mark>ellite communica</mark> tion <mark>s over o</mark> th		nunica		
	Unit: 2		'AL ASPECTS OF SATELLITE SYSTE				Hours
			etermination- orbit perturbations- Orbital o		nation-	launch	es
ana	Unit: 3	es- orbital elle	cts in communication systems performance THE SPACE SEGMENT	e.		10 I	Hours
Intro		recraft subsyst	ems- attitude and orbit control systems- Te	lemetry	- track		
			munication subsystems.	remetry	- track	ing and	
	Unit: 4	I Sol	SATELLITE LINK DESIGN	18	7	18 F	Hours
Basi	c transmission	n theory- syste	em noise te <mark>mperature and G</mark> /T ratio- Design	n of dov	n link	s- up lii	nk
	gn- design of	satellite link fo	or specified C/N.			-	
	Unit: 5		LICATIONS OF SATELLITE SYSTEM				Hours
			AT- GSM- GPS- INMARSAT-Direct Bro			`	/
)- Digital audio broadcast (DAB)- World s	space se	rvices-	Busine	ess
1 V (.	BTV)- GRAN	/ISA1.	Total Laster	II		00.1	T
			Total Lectur	e Hour	S	90 1	Hours
Text	t Books				,		
1	Timothy Pra willey, 2006		stian,JeremyAllnutt, Satellite Communicat	ions, 2 ⁿ	^d editio	n, John	1
2		· .	derhoud and R. A. Nelson, Satellite Commercearson educational pblishers, New Delhi, 2		on syst	ems	
Refe	erence Books						
1	Dennis Rodo	dy, Satellite Co	ommunications, 3 rd edition, Mc Graw Hill,	Interna	tional,	2001.	
2		•	e Communications,4 th edition, Khanna				lhi,

]	Rela	nted Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]	
	1	https://nptel.ac.in/courses/117/105/117105131/	
	2	https://www.youtube.com/watch?v=hXa3bTcIGPU	
	3	https://www.youtube.com/watch?v=BvjlBpP4zU8	

Course Designed By:

Dr.K.Venmathi ,Assistant professor,L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

		Mapping with Program Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO				
CO1	S	L	L	L	L	L	L	L	L	I				
CO2	S	M	L	M	M	L	L	L	L	N				
CO3	S	M	M	L	L	L	L	L	L	I				
CO4	S	S	M	M	M	M	L	L	L	N				
CO5	S	L	L	L	L	F	L	L	L	N				



SCAADATED:18.06.2024 ,REVISED JUNE 2024											
Course code	6EI	INDUSTRIAL AND POWER ELECTRONICS	L	T	P	C					
Core/E		Elective-III –I	6			3					
Pre-re	quisite	Basic knowledge of Electronic Circuits or permission of instructor	abus sion								
Course Object											
To enableTo developeconcept	ents the princi le the student lop the circui ofindustriale	iples and applications of industrial andpowerelectrons is to learn and design industrial and powerelectronics to designing skills related to the power electronics and dectronics	ircuit		odthe						
Expected Cou											
		ion of the course, student will be able to:									
concept in	dustrial elect	designing skills power electronics. Understood the cronics system design.			K	1					
2 Acquire ki		out fundamental concepts and techniques used in pov	ver		K.	2					
	analyze vario stand their ap	ous single phase and three phase power converter circles oplications.	cuits		K.	3					
4 Foster abil		y basic requirements for power electronics based des	ign		K	4					
		ild, and troubleshoot power electronics circuits.			K.	5					
		derstand; K3 - Apply ; K4 - Analyze ; K5 - Evaluate ;	K6 –	Creat	e						
Unit:1		THYRISTORS		4	15ho	ours					
		Construction, Operation and Characteristics of SCR - SBS - LASCR.	– Tw	o Trai	<mark>is</mark> istoi						
Unit:2		TURN ON/OFF MECHANISMS	7		15 h	ours					
Thermal Trigge Off Methods: I Commutation—	ering— Radiat Natural Com	irn on Methods: AC Gate Triggering: Forward it ion Triggering DC Gate Triggering: Pulse Trigger mutation – Forced Commutation: Self Commutation – Thyristor Rating	ing - on -	- Type	es of Trnal P	Furn Pulse					
Unit:3	7. 1 1	CONTROLLED RECTIFIERS & INVERT		45		ours					
Inductive Load	d -Single p	Half Wave Controlled Rectifiers with Resistive I chase Full Wave Controlled Rectifiers with Resistive I Controlled Rectifiers with Resi	tive,l								
Unit:4		CYCLO CONVERTERS AND CHOPPER			15h						
		Centre Tapped Step-Up Cyclo Converter – Single Phar – Three Phase to Single Phase Cyclo converter — S									
Unit:5		APPLICATIONS			15 h	ours					
		nting – Induction Heating – SMPS – UPS – Static Cir y Lighting System – Time Delay Control – StaticSw			er –						

Total Lecture hours

75 hours

Text	Book	(2)
ILAL	DOOR	(3)

- 1 MDSingh,-PowerElectronics, 2ndEdition, Tata-McGrawHill, 2007.
- 2 M.Ramamoorthy, 'ThyristorandtheirApplications, 2ndEdition, EastWestPvt.Ltd, 1999

Reference Books

- 1 Harish C Rai, "Industrial and Power Electronics" 10th edition, Umesh publications 2002
- 2 Timothy J Maloni, "Industrial Solid State Electronic Devices and Circuits" 2nd edition 1986

Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]

- 1 https://www.youtube.com/watch?v=1Auay7ja2oY
- 2 https://www.youtube.com/watch?v=oqnLQVFaqYI
- 3 https://www.youtube.com/watch?v=naxnRkOfh2Q

Course Designed By:

Dr.K. Venmathi , Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr. S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.

Mappi	ng with P	rogr <mark>amn</mark>	<mark>1e</mark> Outc	omes		AL.	7			
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
CO1	S	S	S	M	M	M	S	L	L	S
CO2	S	S	S	M	M	M	S	L	L	L
CO3	S	S	S	M	M	4	L	S	S	M
CO4	M	M	M	S	S	S	L	L	M	M
CO5	M	M	S	S	S	L	M	M	S	S

~ ~ ~	6EJ	ROBOTICS	L	L T P			
Core/Elective/		Elective III –J	6			3	
Supportive:							
Pre-Requisite:		Basic Electronics		abus sion	2023	3-24	
Course Objective	es:						
The Main Object	tives of this	s course are to:					
		dents with the applicationsofRobots					
To know a	bout the sea	nsors, actuators used inRobotsdesigning	5				
Expected Course	Outcomes	:					
		of the course, student will be able to:					
1 scribe tl	he working	concept and types of Robots		K	3		
	kno <mark>wled</mark> ge	of types of sensors and actuators		K	2		
3 e Progra	am <mark>ming Lai</mark>	nguages for Robot design models	/	K	6		
		pt of Mobile Robotic Locomotion			2		
		lications of Robots			3		
K1:Remember;		erstand; K3-Apply; K4-Analyze; K5	-Evaluat		K6 -C ₁		
Unit:1	46	FUNDAMENTALS OF ROB <mark>OT</mark> S			15 hou	urs	
- Actuation Syste		gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS		dinate			
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cup	ms - Contro bes of End F s - Magneti	gnition Sub System: Rob <mark>ot Classifi</mark> ca <mark>tio</mark> ol Method - Program Method.	Types o	rdinate 1 of Grip Scoop	Systematics Show per so other states of the	ems rs	
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cupp Miscellaneous Design	ms - Contro bes of End F s - Magneti	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in	Types o	rdinate 1 of Grip Scoop Selec	System 5hou per os oth tions	ems rs er and	
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cup Miscellaneous Do Design Unit:3	pes of End Es -Magneti	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS	Types of Hook -	of Grip Scoop Selec	System Sy	ems rs er and urs	
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cup Miscellaneous Do Design Unit:3 Sensor Classifica Robotics: Tactile Sensor based Sys	pes of End F s -Magneti evice - Too ation - Inter e Sensors - stems - Use	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in	Types of Hook - Gripper on System Robotin Robot	of Grip Scoop Selection - Selection - Sele	Systematics System	ems rs er and urs rs in and	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cupp Miscellaneous Do Design Unit:3 Sensor Classifica Robotics: Tactile Sensor based Sys	pes of End Fest - Magnetic evice - Too externs - Use aulic Actua	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misses of Sensors in Robotics - Actuators in Rob	Types of Hook - Gripper on System Robotin Robot	of Grip Scoop Selection - Selection - Sele	Systematics System	ems rs er and urs rs in and natic	
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cup Miscellaneous Do Design Unit:3 Sensor Classifica Robotics: Tactile Sensor based Sys Actuators - Hydra Unit:4	pes of End Fest - Magnetic evice - Too extra - Interest Sensors - Stems - Use aulic Actua	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - ILE ROBOTICS LOCOMOTION	Types of Hook - Gripper on System Robot AC Mot	of Grip Scoop Selection - Selection - Sele	Systematics Systematics of Systemati	ems ers er and ers in and natic ers	
- Actuation Syste Unit:2 Introduction: Typ - Vacuum Cupp Miscellaneous Do Design Unit:3 Sensor Classifica Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Cons	es of End Es - Magnetic evice - Too estion - Interested Sensors - Stems - Use aulic Actua MOBI Uses for ideration for	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors -	Types of Hook - Gripper on System Robot AC Mot	of Grip Scoop Selection - Selections.	Systematics System	ems rs er and urs s in and natic urs nd	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cup Miscellaneous Do Design Unit:3 Sensor Classificat Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Const Two Legs Biped	pes of End Formation - Interestems - Use aulic Actual MOBIO Uses for ideration for Four Legs	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Mister of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - LE ROBOTICS LOCOMOTION Locomotion - Leg Mobile Robots - In Dynamics - Types of Legged Robot	Types of Hook - Gripper on System Robot AC Mot	rdinate 1 of Grip Scoop Select em - Select ics - Heors. figuration - of Whe	Systematics System	ems rs er and urs rs in and natic urs nd eg;	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cup Miscellaneous Do Design Unit:3 Sensor Classificate Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Cons Two Legs Biped Mobile Robots Unit:5	pes of End Fest - Magnetic evice - Too externs - Use aulic Actua MOBIO - Uses for ideration for Four Legs	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - ILE ROBOTICS LOCOMOTION Locomotion - Leg Mobile Robots - It or Dynamics - Types of Legged Robot is (Quadruped) - Six Legs (Hexapod) - Company Com	Types of Hook - Gripper on System Cellaneous AC Moto Leg Con Locomo Concept of	of Grip Scoop Selection - Selection - Sele	System Sy	ems rs er and urs es in and actic urs nd eg;	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cupe Miscellaneous Do Design Unit:3 Sensor Classifica Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Cons Two Legs Biped Mobile Robots Unit:5 Industrial Applica Assembly Applica	es of End For Service - Too ation - Interest Sensors - Stems - Use aulic Actua MOBI Uses for dideration for Four Legs cations: Macations - Inservice - Inservi	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - ILE ROBOTICS LOCOMOTION Locomotion - Leg Mobile Robots - It or Dynamics - Types of Legged Robot is (Quadruped) - Six Legs (Hexapod) - Company of Co	Types of Hook - Gripper on System Robot AC Mot Locomo Concept of the Actions - Arc Weld	rdinate 1 of Grip Scoop Select em - Select em - Select figurat tion - of Whee Arc V ling -	System Sy	ems rs er and urs s in and natic urs nd eg;	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cupe Miscellaneous Doesign Unit:3 Sensor Classificate Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Constant Two Legs Biped Mobile Robots Unit:5 Industrial Applications - Robotics	pes of End Fest - Magnetic evice - Too extra - Use aulic Actua MOBI - Uses for ideration for - Four Legs - Eations - Insulations	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - ILE ROBOTICS LOCOMOTION Locomotion - Leg Mobile Robots - It or Dynamics - Types of Legged Robot is (Quadruped) - Six Legs (Hexapod) - Construction of Robotics of Applications - Robotics for Application of Robotics for Application of Robotics for Application of Robotics for Application of Robotics	Types of Hook - Gripper on System Collaneo in Robot AC Mot Locomo Concept of the	of Grip Scoop Selection - Sele	System Sy	ems rs er and urs s in and natic urs nd eg;	
- Actuation System Unit:2 Introduction: Typ - Vacuum Cupe Miscellaneous Doesign Unit:3 Sensor Classificate Robotics: Tactile Sensor based System Actuators - Hydra Unit:4 Introduction Key Assembly - Constant Two Legs Biped Mobile Robots Unit:5 Industrial Applications - Robotics	pes of End Fest - Magnetic evice - Too extra - Use aulic Actua MOBI - Uses for ideration for - Four Legs - Eations - Insulations	gnition Sub System: Robot Classification of Method - Program Method. ROBOT END EFFECTORS Effectors - Mechanical Grippers - Other ic Gripper - Adhesive Gripper - Hols as End Effectors - Considerations in SENSORS IN ROBOTS rnal Sensors - External Sensors - Visit Proximity and Range Sensors - Misters of Sensors in Robotics - Actuators in tors - Electric Actuators - DC Motors - ILE ROBOTICS LOCOMOTION Locomotion - Leg Mobile Robots - It or Dynamics - Types of Legged Robot is (Quadruped) - Six Legs (Hexapod) - Company of Co	Types of Hook - Gripper on System on System on Robot AC Mot Leg Con Locomo Concept of the Concep	rdinate Inf Grip Scoop Select Em - Select	System Sy	ems rs er and urs s in and natic urs nd nd eg; urs	

Text	Book(s)
1	M.P.Groover,Mitchellweiss,Roger.N.Nagel,NicholasG.Odrey, Industrial Robotics— Technology,programmingandApplication I,McGraw-Hill, 2008.
2	Ghosh,—Control in Roboticsand Automation:SensorBased Integration", Allied Publishers, Chennai,1998.
Refe	rence Books
1	Deb. S.R., — Robotics Technology andflexible Automation , John Wiley, USA 1992.
2	KlafterR.D., ChimielewskiT.A., Negin M., —Robotic Engineering—Anintegrated approach, Prentice Hall of India, New Delhi, 1994.
Rela	ted Online Contents [MOOC, SWAYAM, NPEL, Website etc.]
1	https://nptel.ac.in/courses/112/105/112105249/Introduction to Robots
2	https://nptel.ac.in/courses/112/101/112101098/Robotics and Automation
	Course Designed By: Dr.K.Venmathi ,Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode

			The second second			A				
Mappi	ng with P	<mark>rogr</mark> amn	<mark>1e Outc</mark>	omes			2	701-		
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10
	Δ			-1	7	133				
CO1	S	S	M	M	M	M	S	M	M	S
A		10		gw						
CO2	S	M	M	M	M	M	S	L	L	L
				100		Charles		7	9	
CO3	S	M	S	M	M	L	L	L	M	M
	1 /	CA.		The		Times		25		1.
CO4	S	S	M	S	S	S	L	Lio	M	M
		37	300		Colimbi	rtore		60		
CO5	S	S	S	S	M	L	M	M	S	M
			~ (3)20		- 0 W	IL SO			

Course 6EK	PROGRAMMABLE LOGIC CONTROLLERS	$\mathbf{S} \mid \mathbf{I}$	T	P	C			
code			<u> </u>					
Core/Elective/	Elective III – K	3			6			
Supportive:								
Pre-requisit	Digital Electronics and computer Architecture and Organization	Syl	llab	2023	3-24			
		Ve on	ersi					
Course Objectives								
	s of this course are to: nowledge levels needed for PLC programming and operating	ınnu i	t and					
output modul		,p «						
	rudents to create ladder diagrams from process control							
descriptionar	d understand various types of PLCregisters							
	imers and Counters for the control of industrial processes, P	LC						
functionsand	DataHandlingFunctions.							
Expected Course (Outcomes:							
	completion of the course, student will be able to:							
	on Programmable Logic Controllers and will understand			K	2			
	Devices to which PLC input and output modules		k.					
2 Gain knowledge control descripti	about various types of PLC registers, ladder diagrams from	proce	ss	K	2			
3 Develop a coil a	nd contact control system and analog PLC operations			K	[4			
4 Apply time delay	on PLC operations			K	3			
5 Able to use diffe	rent t <mark>ypes PLC functions, data handling functions and i</mark> ts va	rious	7	K	2			
K1 - Remember; I	2 - Understand; K3 - Apply; K4 - Analyse; K5 - Evaluate;	K6 -	Creat	e				
Unit:1	PROGRAMMABLE LOGIC	1	15	hour	rs			
	c – Introduction - Prog <mark>rammable Logic Structu</mark> res - Progran			_				
	grammable Array Logic (Pals), Programmable Gate Arrays	(PGA	S), F	eld				
ProgrammableGate		22.5	1 -					
ProgrammableLogical -Traffic Light Control	c Devices (PLDs) -Design of Sequential Networks Using RC)Ms a	nd Fl	ash				
Unit:2	PROGRAMMABLE LOGIC CONTROLLERS		151	nour	'S			
	icControllers(PLCS) - Introduction Parts Of PLC - Pri	nciple						
Operation-PLCSize		-			-			
Unit:3	BASICS OF PLC PROGRAMMING		15	hour	'S			
PLCProgramming-S				1E				
	OFF Instructions -Electromagnetic Control Relays-MotorStarters -Manually							
	• •	Output		trol				
Devices - Latching Ladder Diagram int	Relays -PLCLadderDiagram - Converting Simple PLC Relay LadderDiagram	ne Re	lay					
Ladder Diagram III	JI Le Reidy Ladder Diagram							

Timer Instructions ON DELAY Timer and OFF DELAY Timer - Counter Instructions - Up/Down Counters -Timer and Counter Applications - Program Control Instructions - Data Manipulating Instructions - Math Instructions

Unit:5	APPLICATIONS OF PLC 15hours									
Applications of PLC - Simple Materials Handling Applications - Automatic Control of										
Warehouse Do	Warehouse Door - Automatic Lubricating Oil Supplier Conveyor Belt - Motor Control									
Automatic Car	Automatic Car Washing Machine - Bottle Label Detection - Process Control Application									

		Total Lecture hours	75 Hours							
Text	Book(s)									
1	Charles H. Roth, Jr "Fundamentals of Logic Design", Fourth Edition, Jaico Publishing									
2	Frank D. Petruzella" Programmable Logic Controllers ", McGraw- Hill book, company,									
3	Siemens "	PLC Handbook ''.								
Refe	rence Books	(6) 00 00 00 00 00 00 00 00 00 00 00 00 00								
1	1. William	n I. Fl <mark>etcher "An Engineering Approach to Digital Design</mark> '',	Prentice, Hall							
	of India L	td., New Delhi, 1999.								
Rela	ted Online C	Contents [MOOC, SWAYAM, NPTEL, Websites etc.]								
1	https://unit	onicsplc.com/what-is-plc-programmable-logic-controller/								
		Course Designed By:								
	Dr.K.Ven	<mark>mathi ,As</mark> sistant professor,L.R.G.Govt Arts <mark>Co</mark> lleg <mark>e for Wom</mark> er	n, Tirupur &							
	Mr.S.	<mark>Shankar</mark> , Assistant Professor, Sri Vasavi Co <mark>lle</mark> ge, <mark>SF Wing,</mark> En	rode.							

Mappi	Mapping with Programme Outcomes												
COs	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	P10			
CO1	S	S	M	M	M	M	S	M	M	S			
CO2	S	M	M	M	M	M	S	L	L	L			
CO3	S	M	S	M S	M	PLUI	H D	L	M	M			
CO4	S	S	M	S	IE SU E	S	L	L	M	M			
CO5	S	S	S	S	M	L	M	M	S	M			

^{*}S-Strong; M-Medium; L-Low

SCAADATED:18.06.2024, REVISED JUNE 2024

Course Code 6EL	VLSI DESIGN	L	T	P	С
Core/Elective/Supportive:	ELECTIVE – III-L	6	0	0	3
Pre-requisite:	Digital Principles and Applications	Syllab versio			2023-24
Course Objectives					

The objectives of this course are:

- ❖ To provide knowledge on Fabrication Process of NMOS,PMOS,CMOS AND BICMOS, Super integration concepts.
- ❖ To develop the skill to analyze the electrical properties of MOS transistor, design stick diagrams and layout diagrams for MOS transistors, contacts andwires.
- ❖ To investigate the effect of floor planning, placement, routing and power delay estimation in physical design of digital circuits and memorydesign.
- ❖ To apply the concept of Combinational and Sequential CircuitTesting.

Expected Course Outcomes:							
On suc	cessful completion of the course, student will be able to:						
1	Gain the knowledge on fabrication principles. K1						
2	Able to analyze the electrical properties of MOS transistors.	K4					
3	Apply the appropriate layout design rule to create a VLSI layout for a design.	K6					
4	Understand the physical design steps and gain the knowledge on types of VLSI design styles.	K2					
5	Gain the knowledge, analyze and apply test principles to evaluate the VLSI designs.	K5					

K1 – Remember; K2 – Understand; K3 – Apply; K4 – Analyze; K5 – Evaluate; K6 – Create Unit: 1 VLSI TECHNOLOGY 15 Hours

Fabrication sequence – process flow – Testing – Super integration concepts – Integrated Passive components – MOS Resistors and capacitors – Crossovers – NMOS – PMOS – CMOS – BICMOS fabrication processes – comparison.

ELECTRICAL PROPERTIES OF MOS Unit: 2 15 Hours **DEVICES**

Drain to source current (I_{ds}) versus Drain to source voltage (V_{ds}) relationships – MOS transistor threshold voltage (V_t) – MOS transistor trans-conductance g_m and output conductance g_{ds} – figure of merit (ω_0) – pass transistor- pull – up to pull – down ratio.

Unit: 3 **DESIGN PROCESSES** 15 Hours VLSI designflowstick diagram design rules withexamples-Designrulesfor Layout

Propagation delays- scaling of MOS circuits – limitations of scaling.

Unit: 4 VLSI PHYSICAL DESIGN AND STYLES 15 Hours

PHYSICAL DESIGN:

Floor Planning – Placement – Routing – Power Delay Estimation – Clock Routing – Power Routing.

diagramsofdigitalcircuits—sheetresistanceR_s—standardunitofcapacitance—Inverterdelays—

VLSI DESIGN STYLES:

Full Custom – Semi custom – Standard Cells – Gate Arrays – FPGAs – CPLDs.

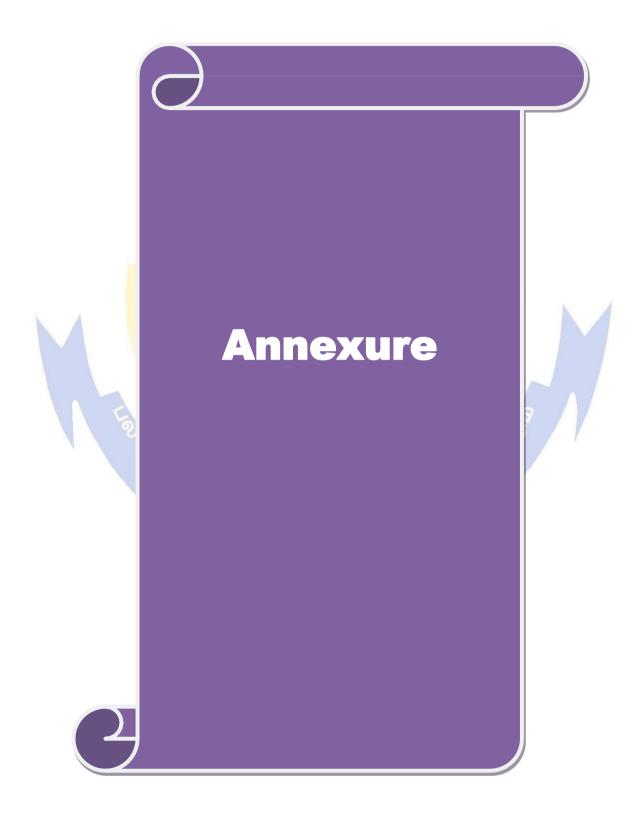
Unit: 5	TESTING OF VLSI CIRCUITS	15 Hours				
Test Principles-BIST-Test Bench- Combinational Circuit Testing, Sequential Circuit Testing,						
Test Bench Techni	ques.					

Total Lecture Hours

75 Hours

Text Books						
1	Basic VLSI Design, Douglas ,3rd Edition, A. Pucknell, Kamran Eshraghian, PHI, New Delhi, 2011.					
2	Modern VLSI design, Wayne Wolf, 3rdEdition, Pearson Education, New Delhi, 4th impression 2008.					
Refere	nce Books					
1	Introduction to VLSI Circuits and Systems, John .P. Uyemura, John Wiley, Student Edition, New Delhi, Reprint 2006.					
2	Principles of CMOS VLSI Design, N.H.E Weste, K.Eshraghian, Adisson Wesley, 2nd Edition, NewDelhi.					
3	Application Specific Integrated Circuits, Michel John Sebastian Smith, Addison Wesley, Indian Edition, 4th Indian Reprint 2001, New Delhi.					
Related Online Contents [MOOC, SWAYAM, NPTEL, Websites etc.]						
1	https://nptel.ac.in/courses/117/101/117101058/					
2	https://www.youtube.com/watch?v=9SnR3M3CIm4					
3	https://www.youtube.com/watch?v=Y8FvvzcocT4					
Course Designed By:						
	Dr.K.Venmathi ,Assistant professor, L.R.G.Govt Arts College for Women, Tirupur & Mr.S.Shankar , Assistant Professor, Sri Vasavi College, SF Wing, Erode.					

A	Mapping with Program Outcomes									
Cos	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10
CO1	S	T	L	L	L	L	L	(L)	L	L
CO2	S	S	L	S	M	L	M	L	L	L
CO3	S	M	S	L	S	L	M	M	L	S
CO4	S	L	M	L	S	L	SVL	L	M	L
CO5	S	S	M	M	S	LIE	L	L	M	S



BHARATHIAR UNIVERSITY :: COIMBATORE 641046 DEPARTMENT OF ELECTRONICS

MISSION

- To develop appropriate facilities for promotingresearch activities.
- To inculcate leadership qualities among students for self and societal growth.
- To nurture students on emerging technologies for serving industry needs through industry institute interface.
- To enrich teaching learning process by transforming young minds to beresourceful engineers.

